

FIG. 1
PRIOR ART

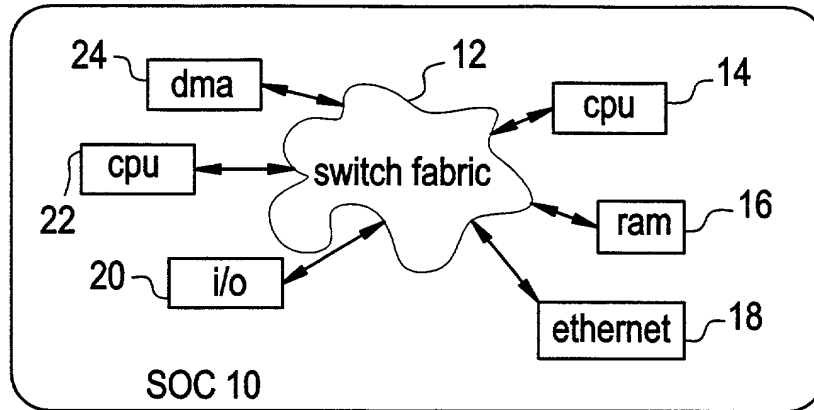


FIG. 2

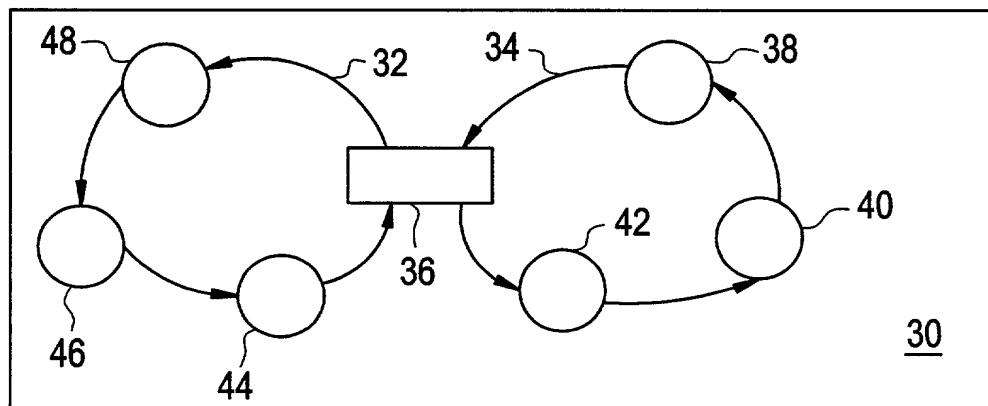


FIG. 3

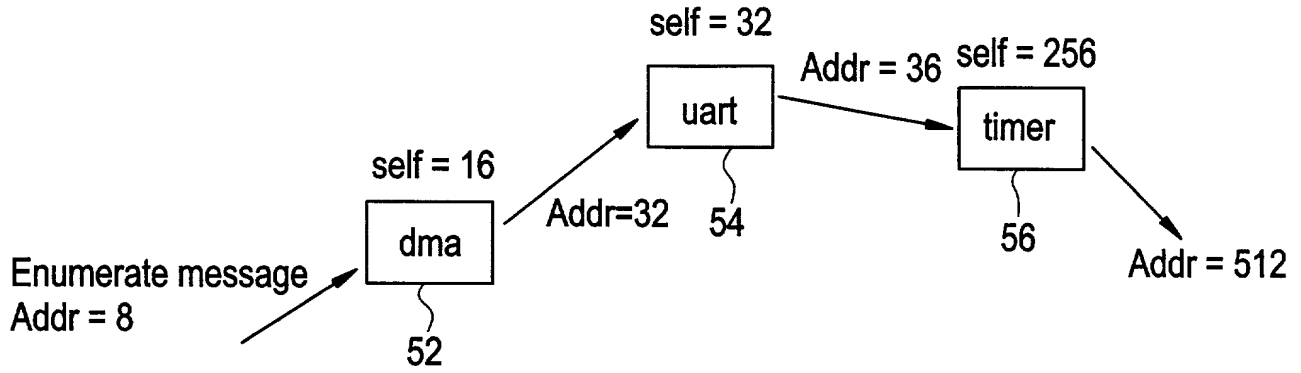


FIG. 4

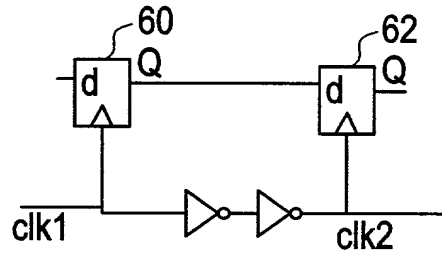


FIG. 5

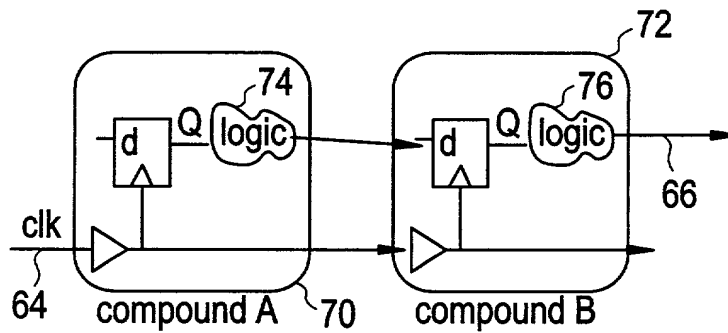


FIG. 6

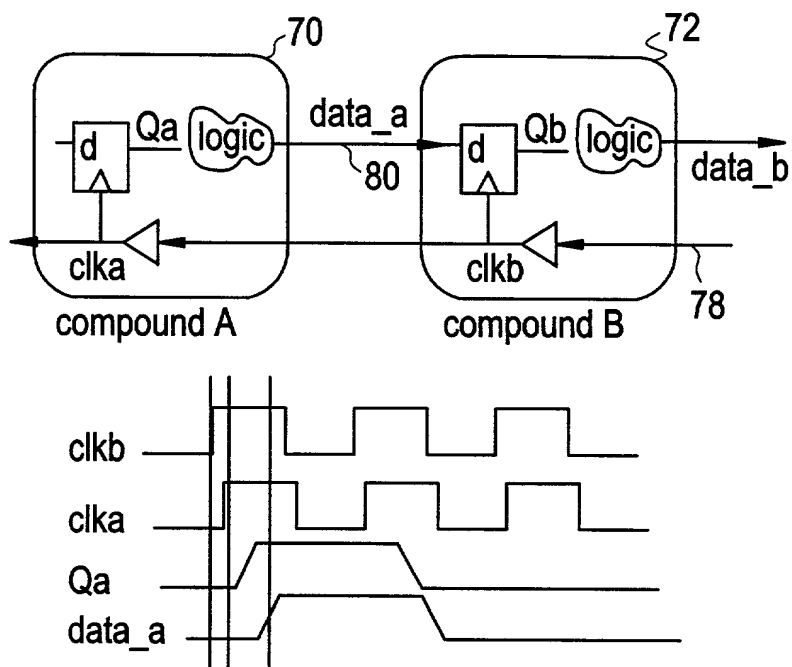


FIG. 7

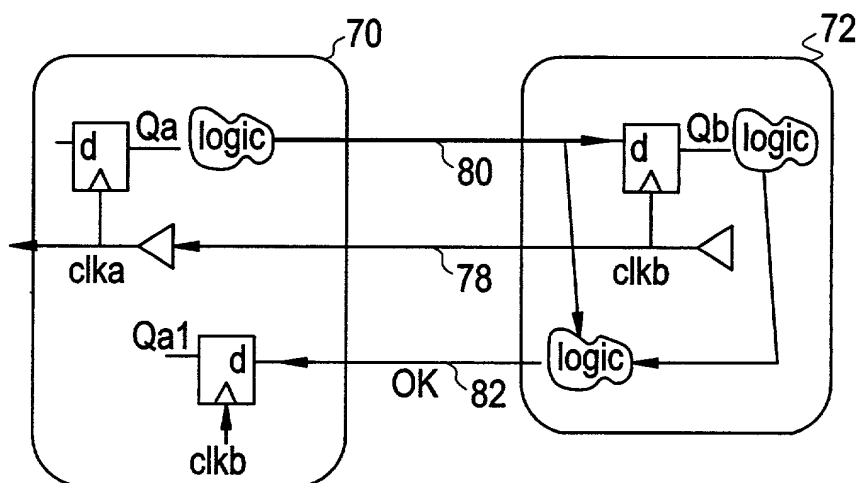


FIG. 8

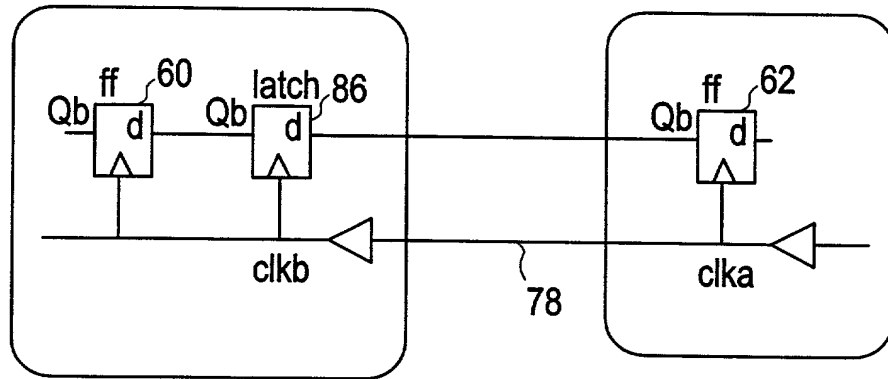


FIG. 9

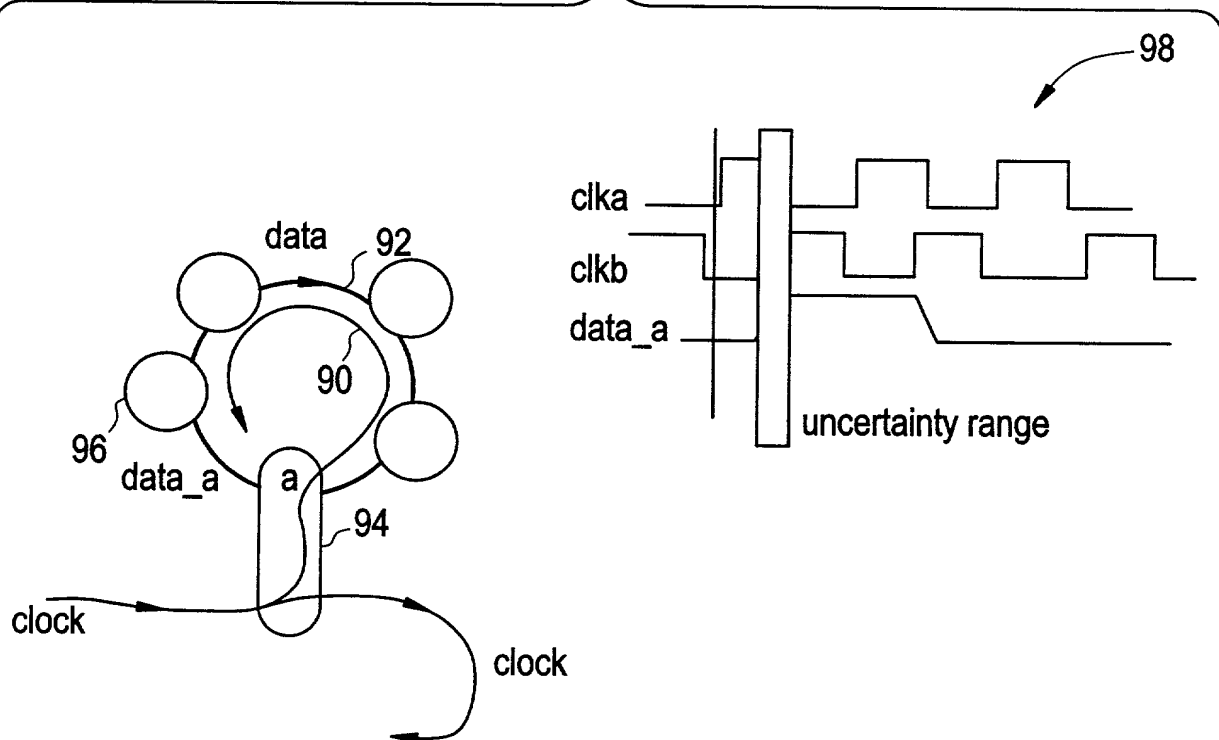


FIG. 10

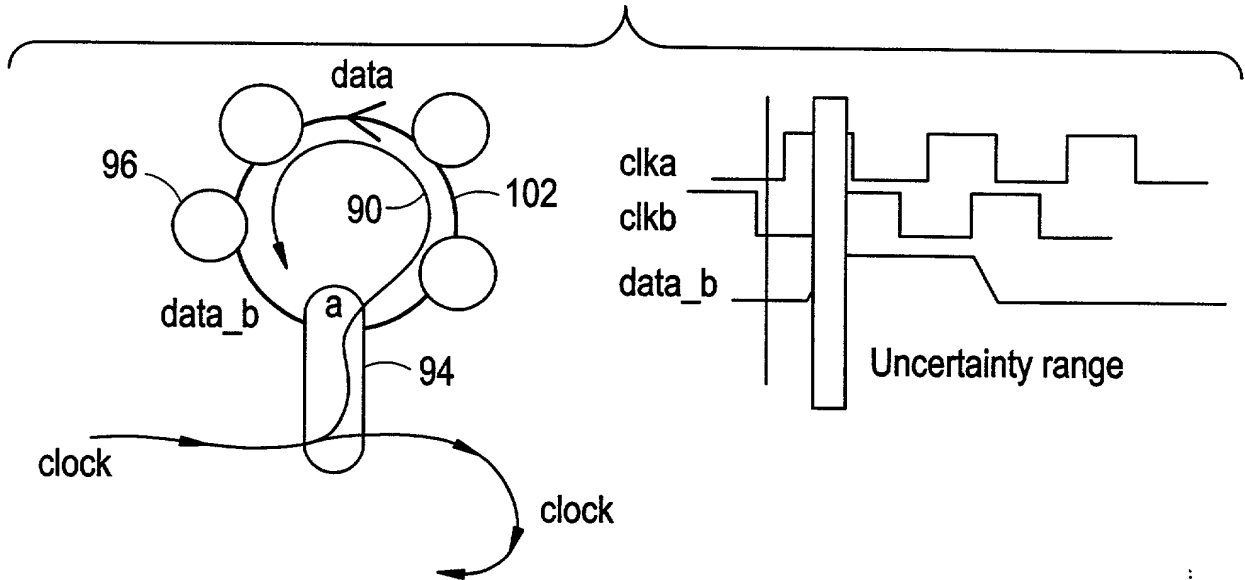


FIG. 11

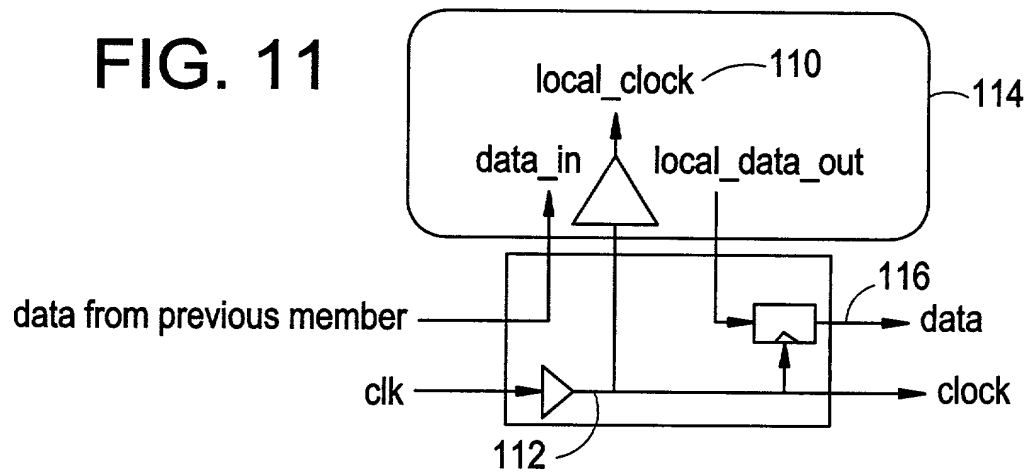


FIG. 12

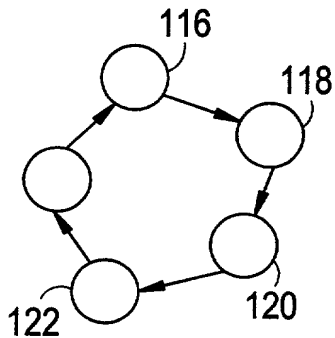


FIG. 13

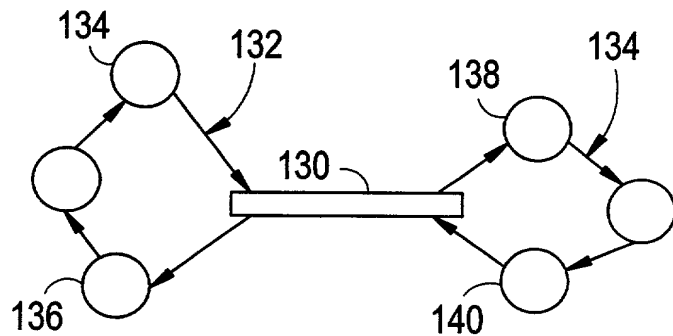


FIG. 14

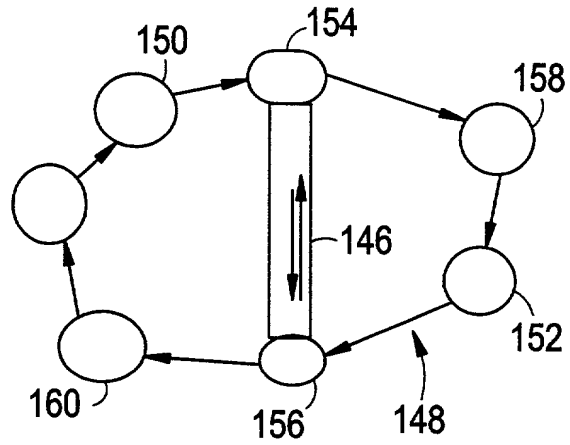


FIG. 15

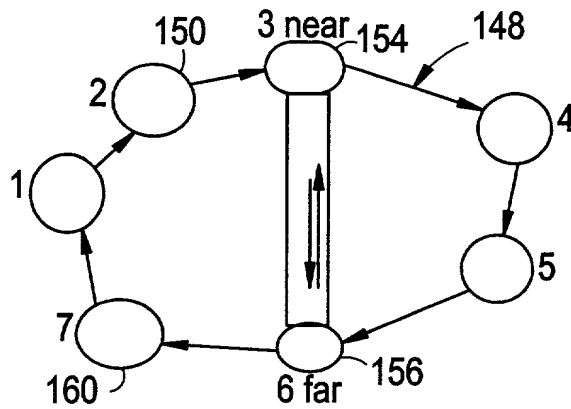


FIG. 16

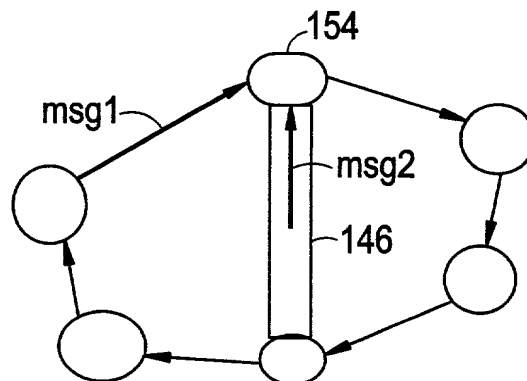


FIG. 17

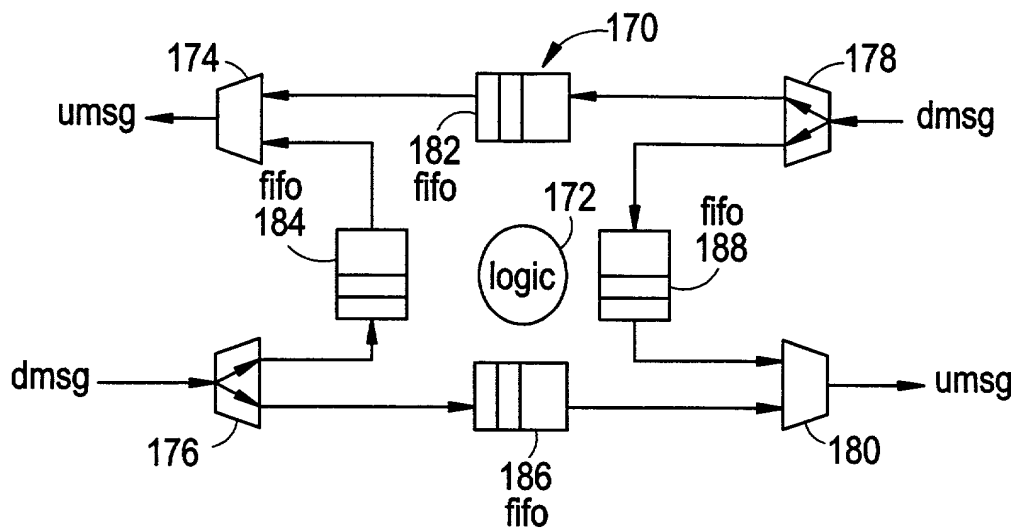


FIG. 18

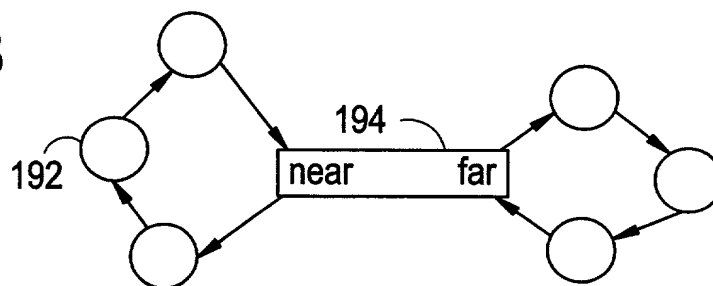


FIG. 19

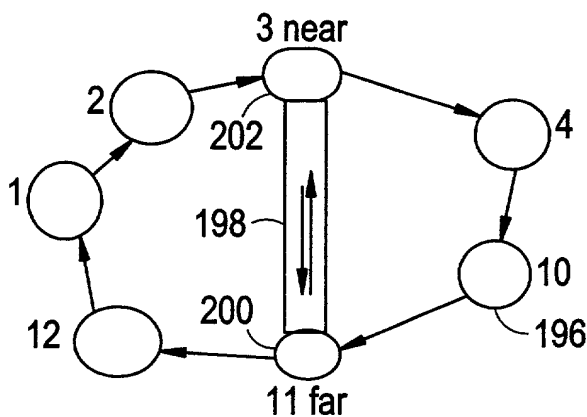


FIG. 20

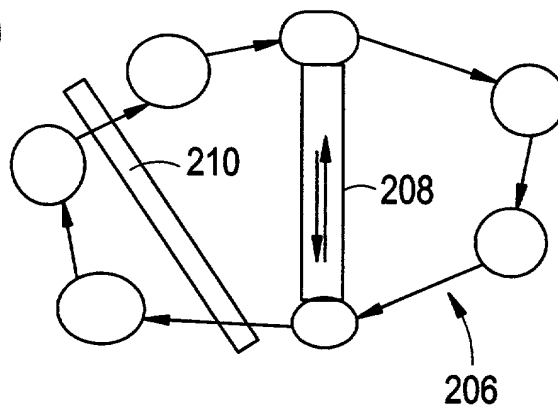


FIG. 21

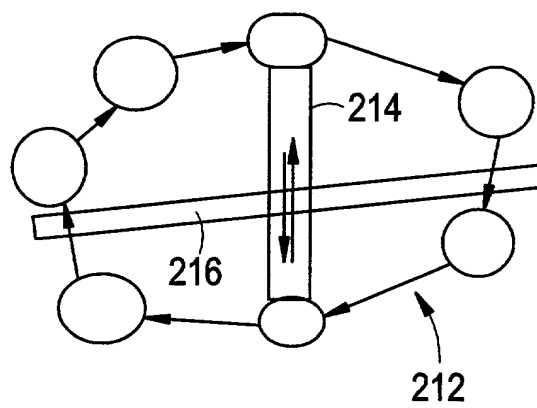


FIG. 22

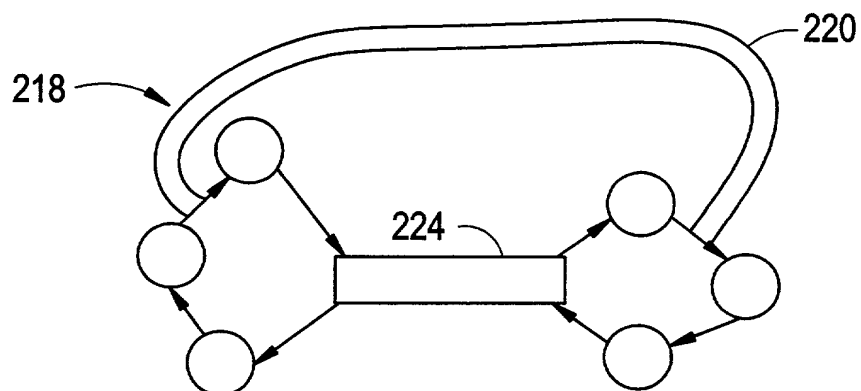


FIG. 23

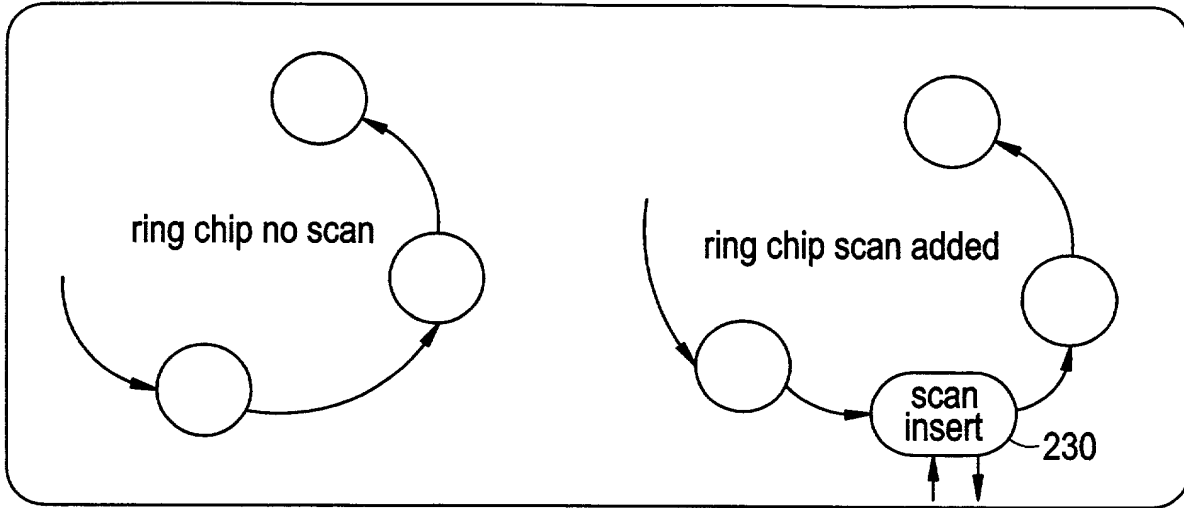


FIG. 24

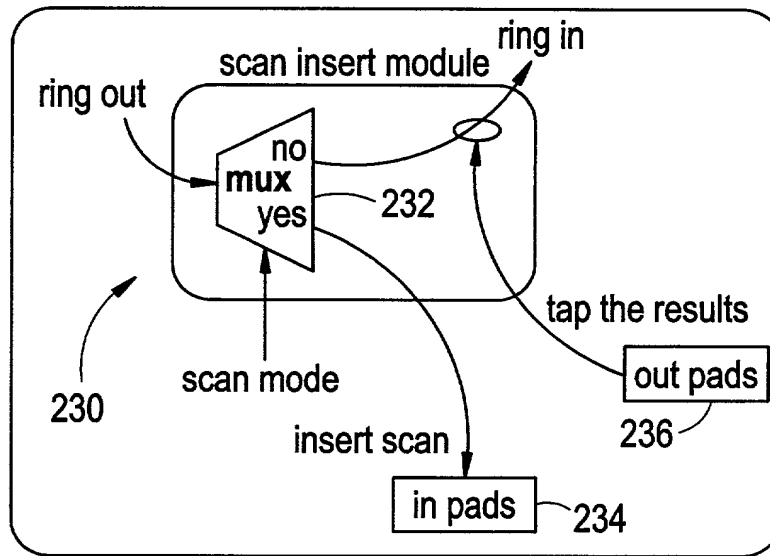


FIG. 25

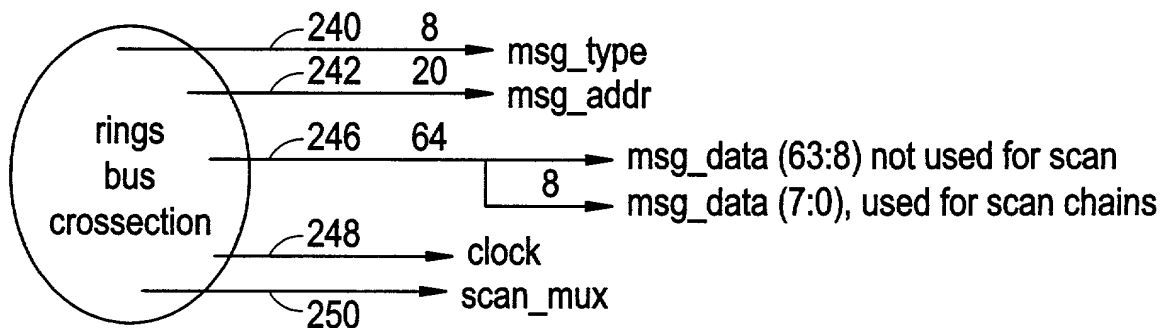


FIG. 26

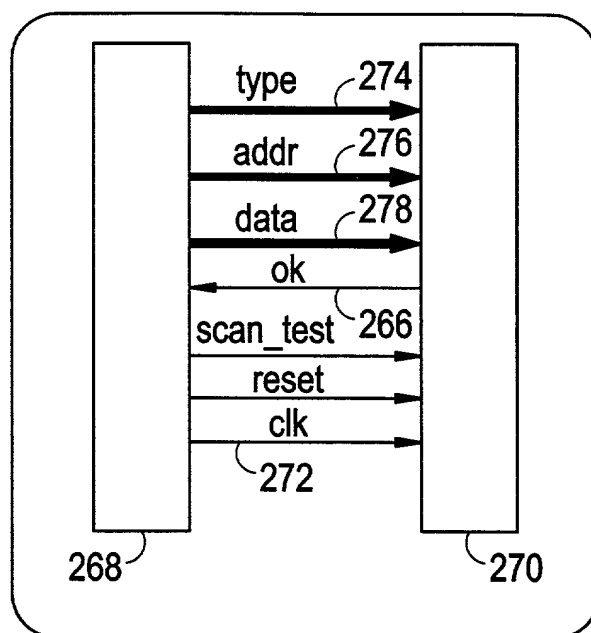


FIG. 27

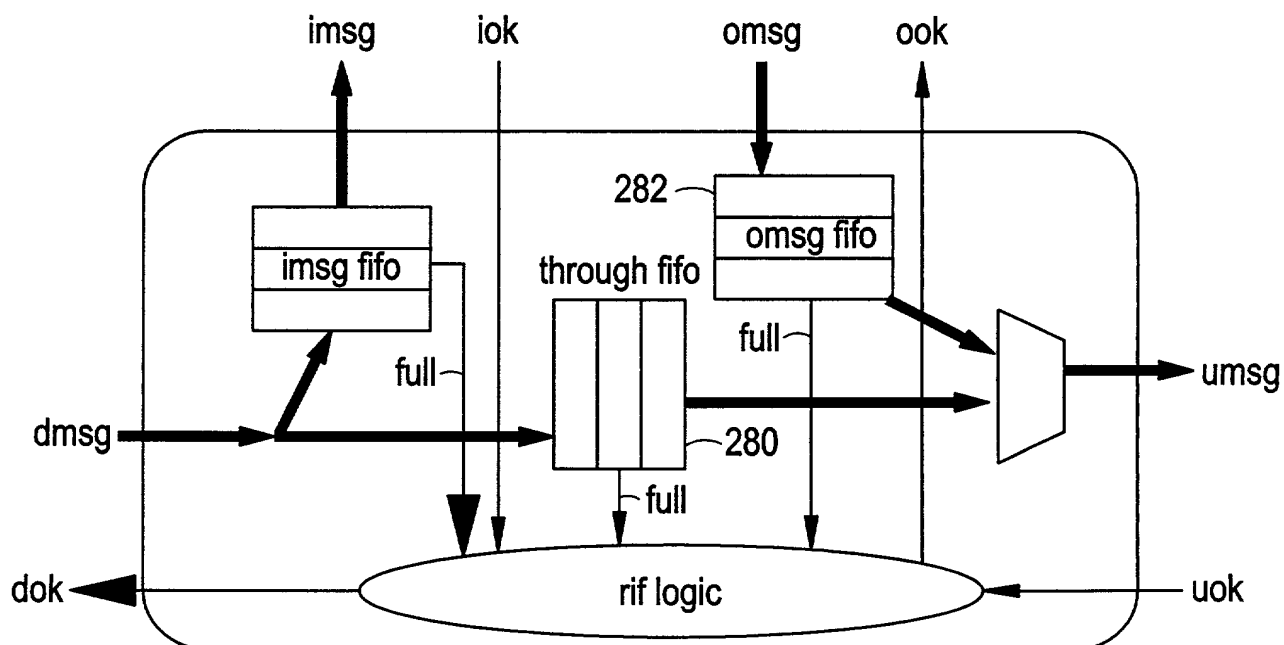


FIG. 28

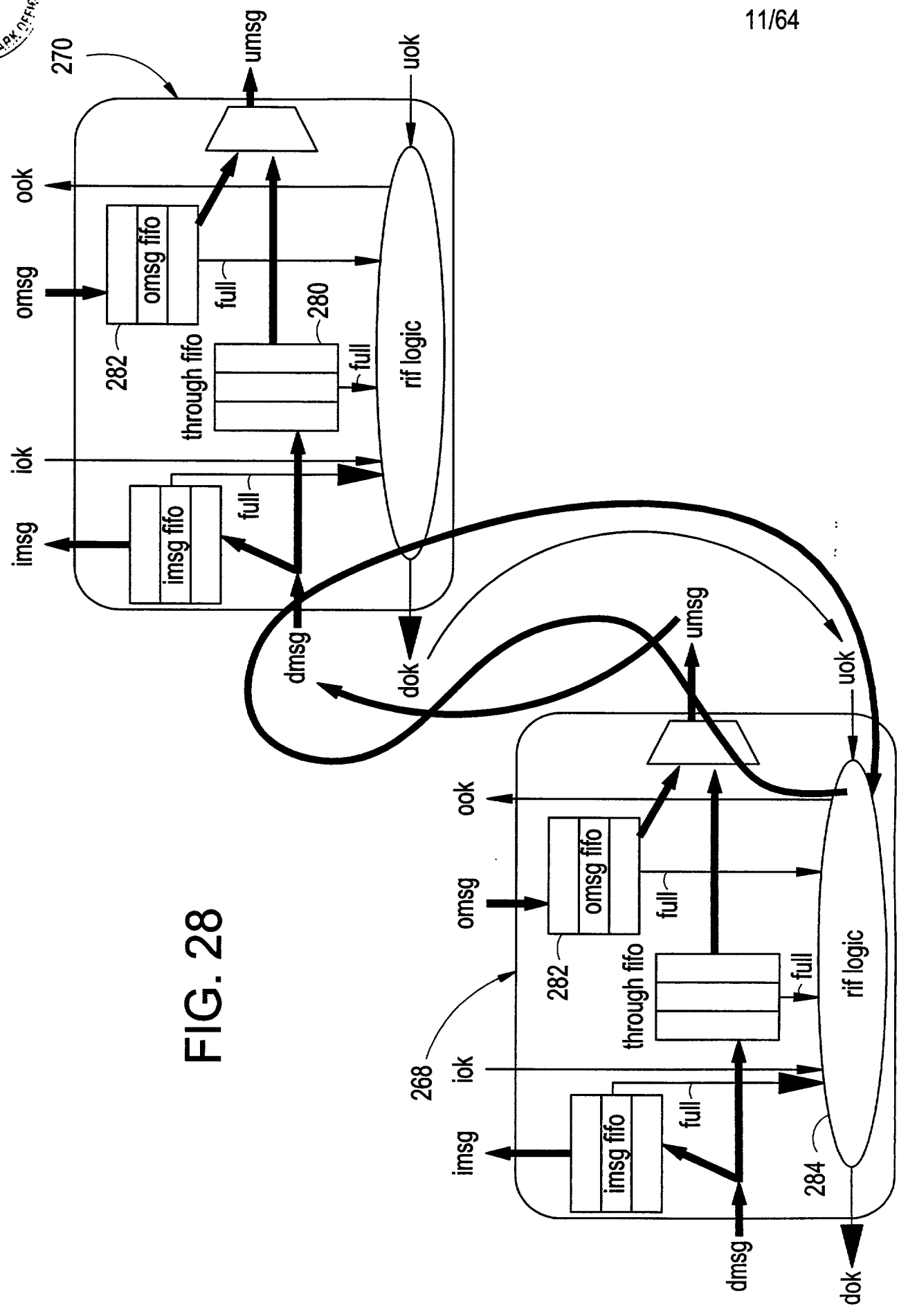


FIG. 29

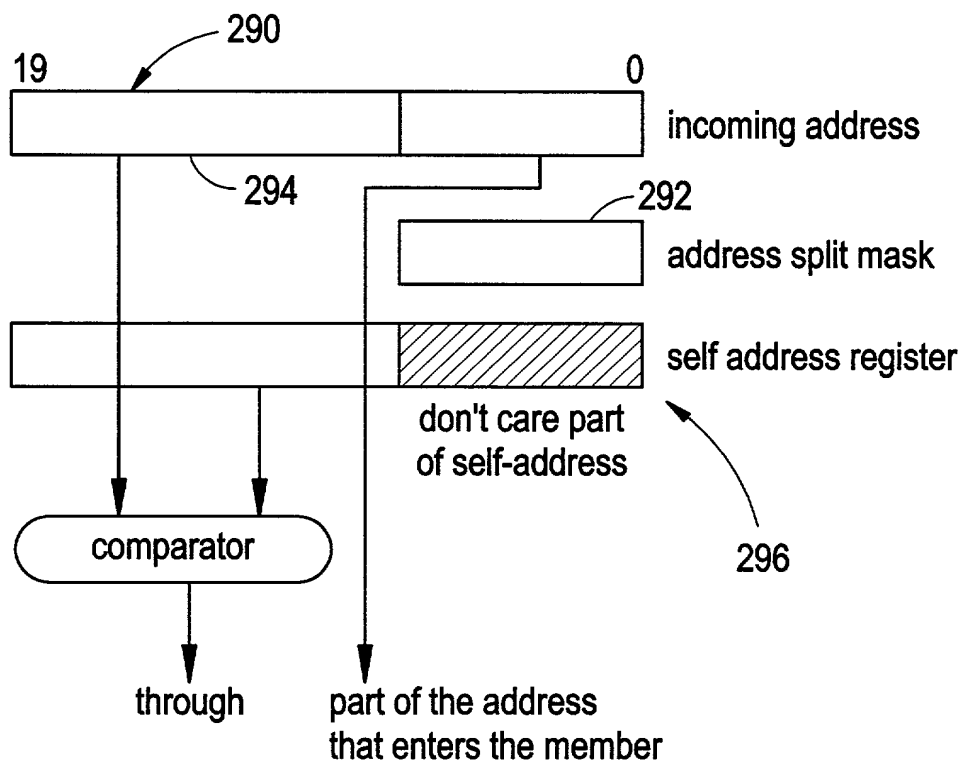


FIG. 30

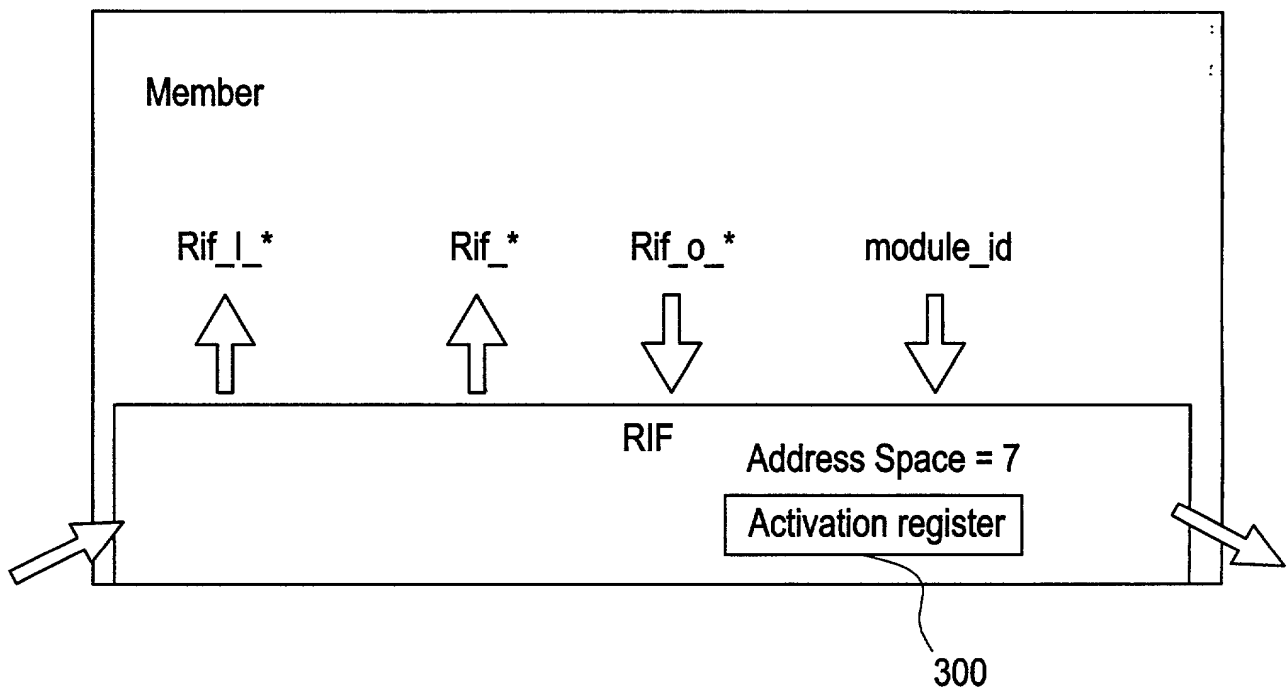


FIG. 31

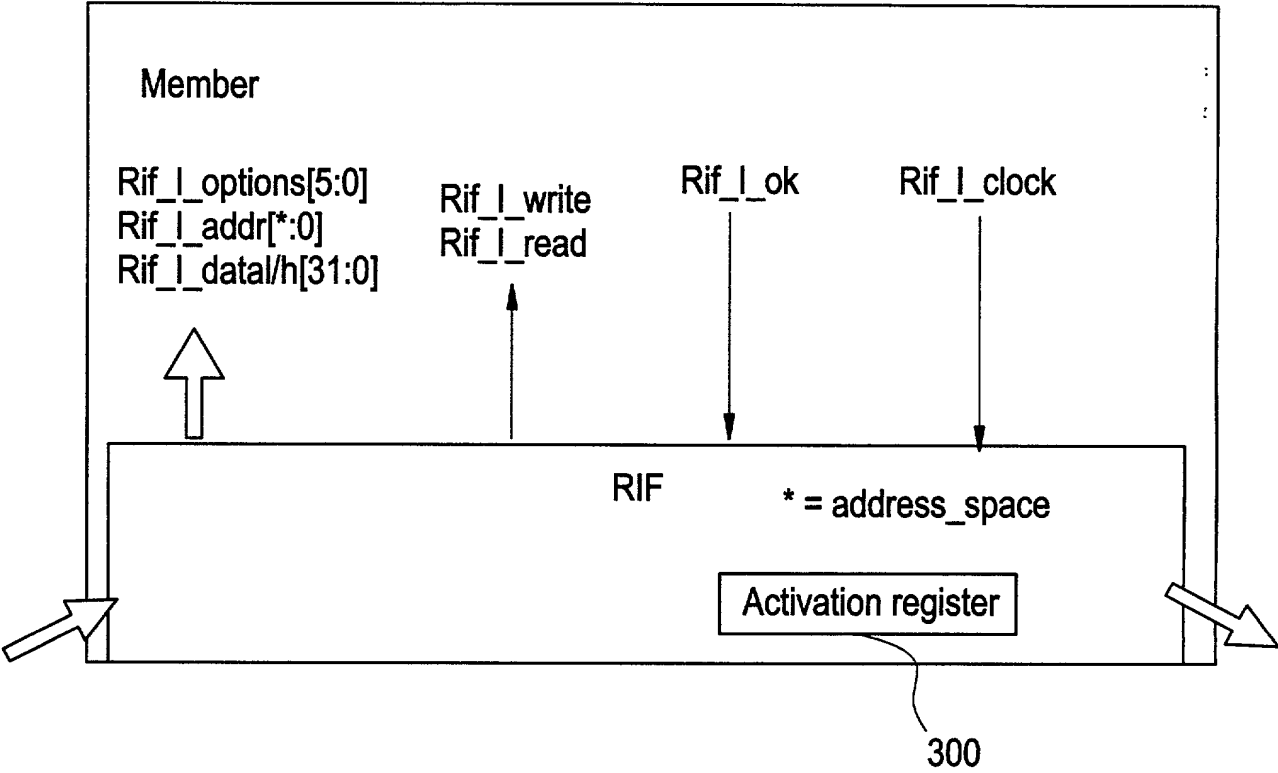


FIG. 32

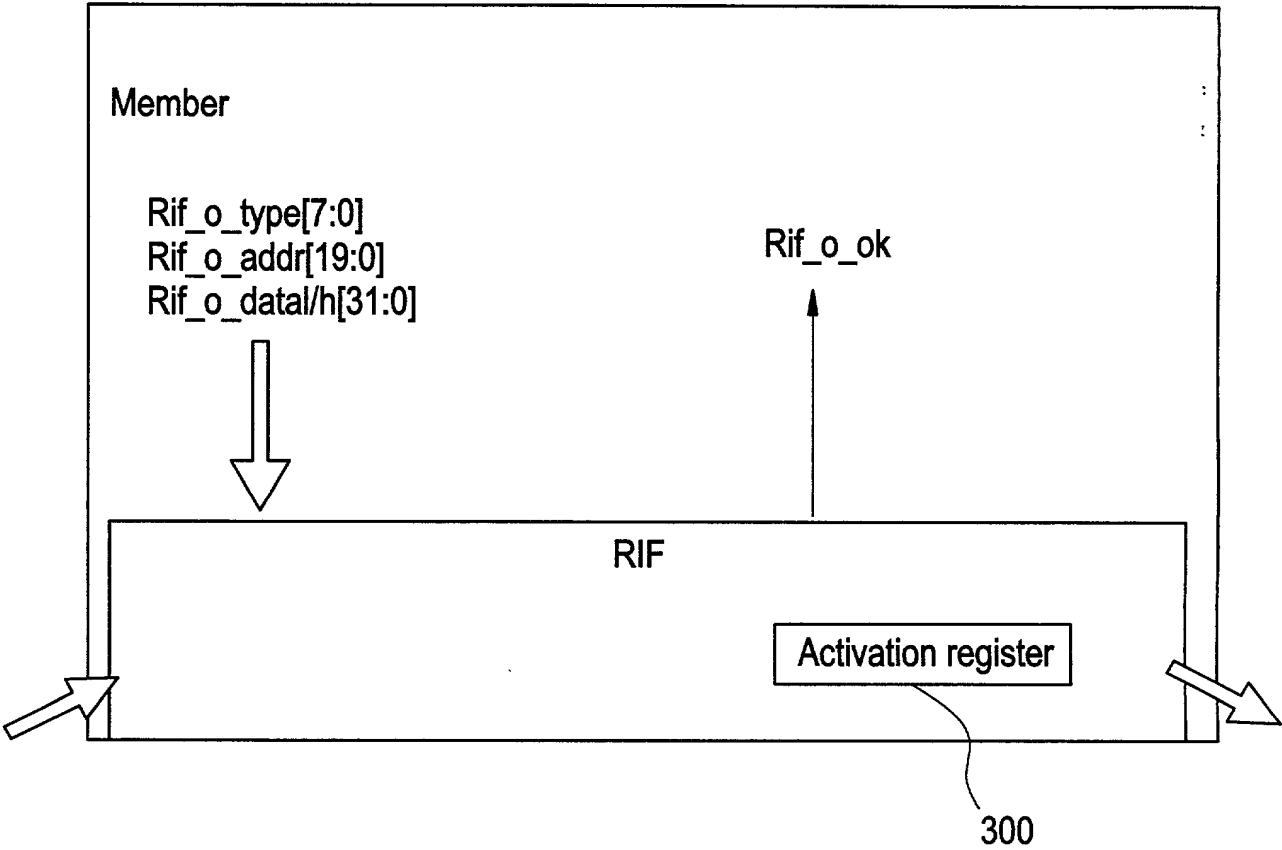


FIG. 33

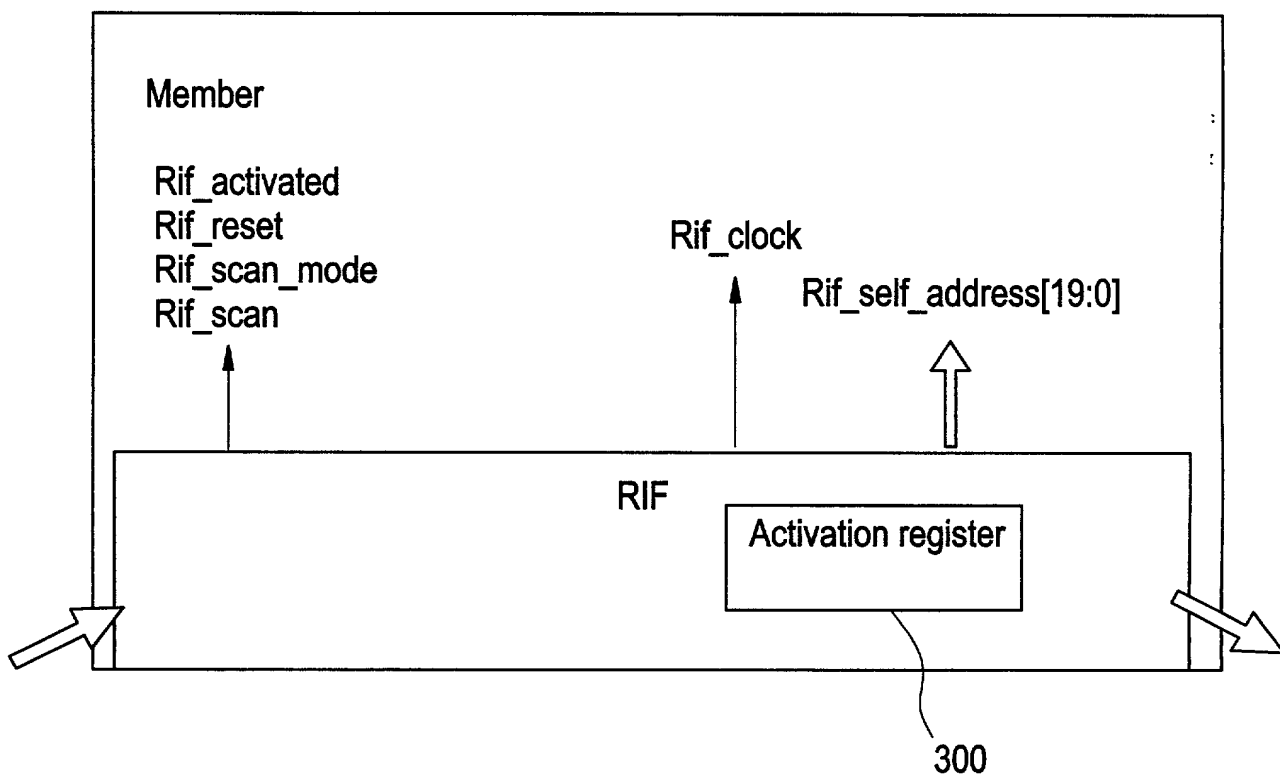


FIG. 34

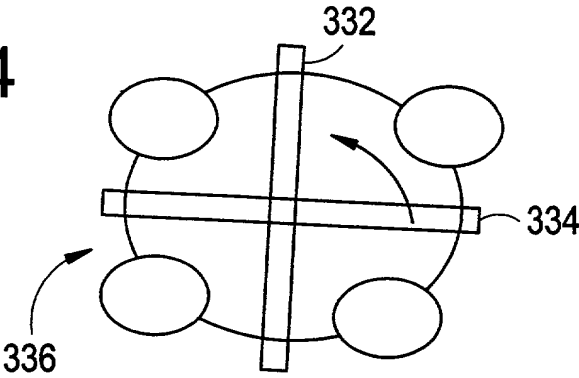


FIG. 35

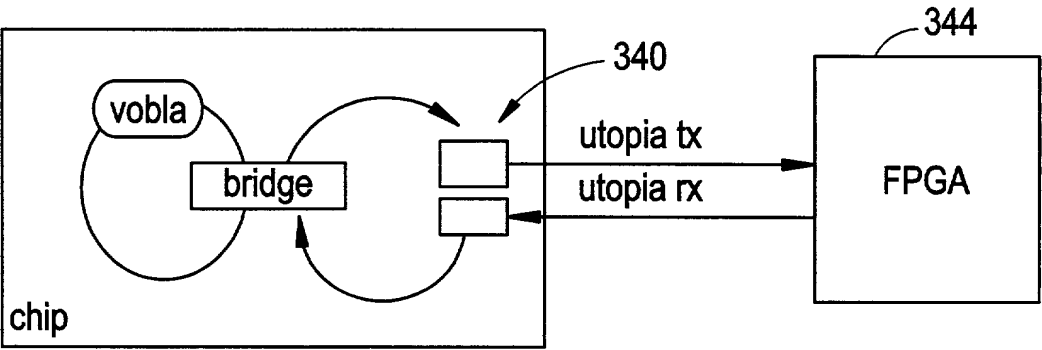


FIG. 36

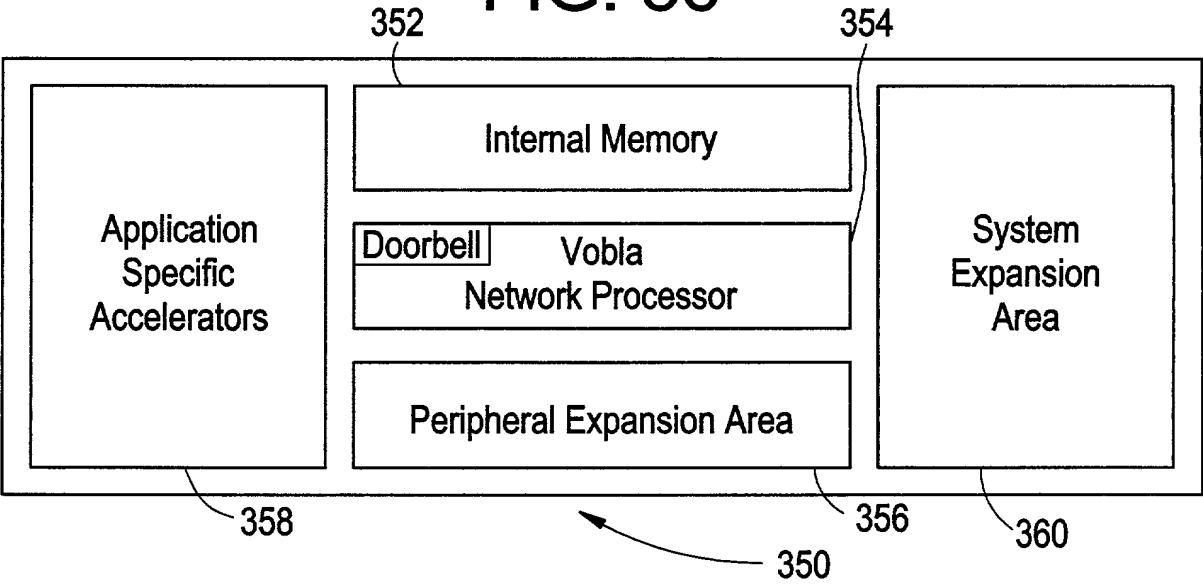
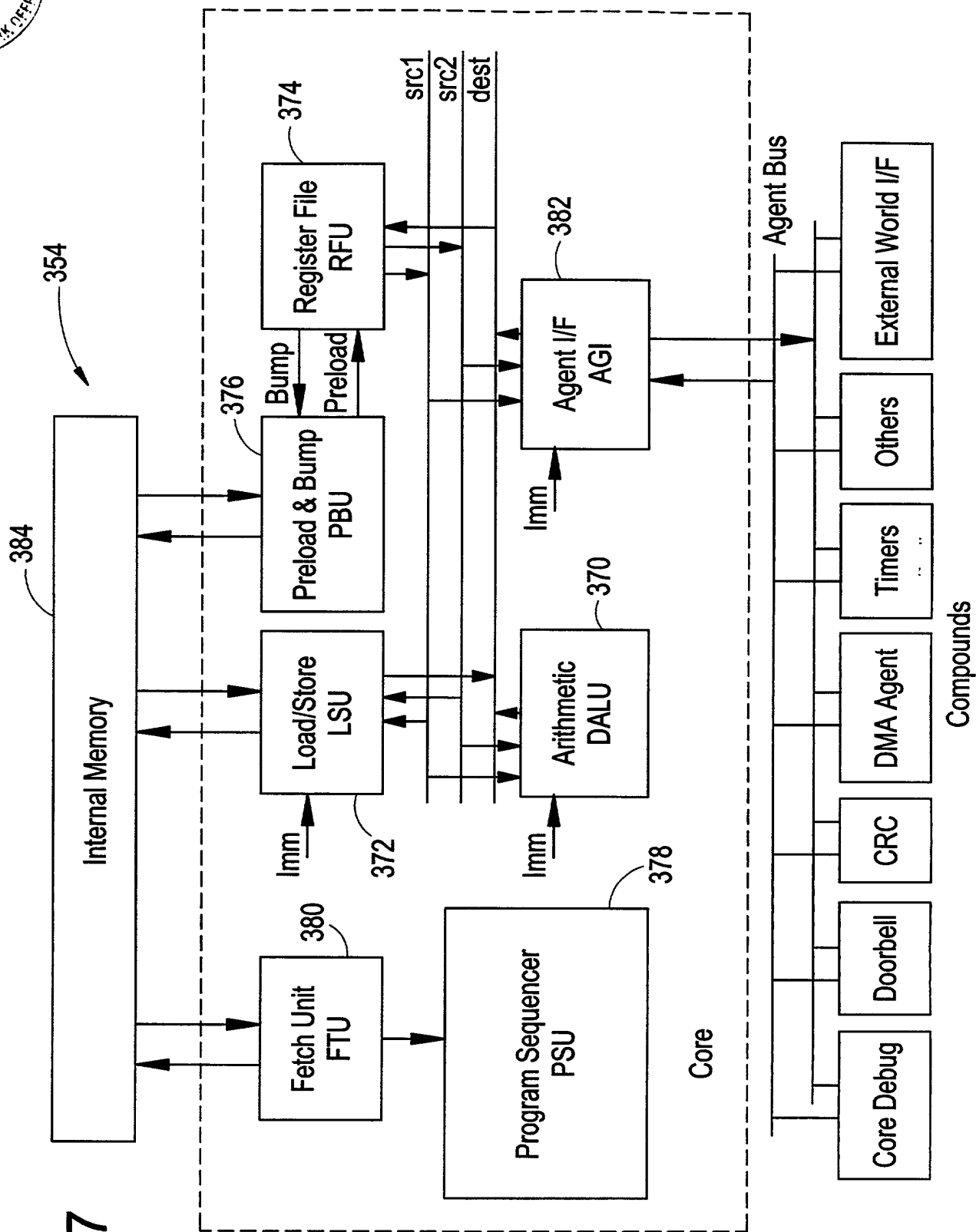


FIG. 37

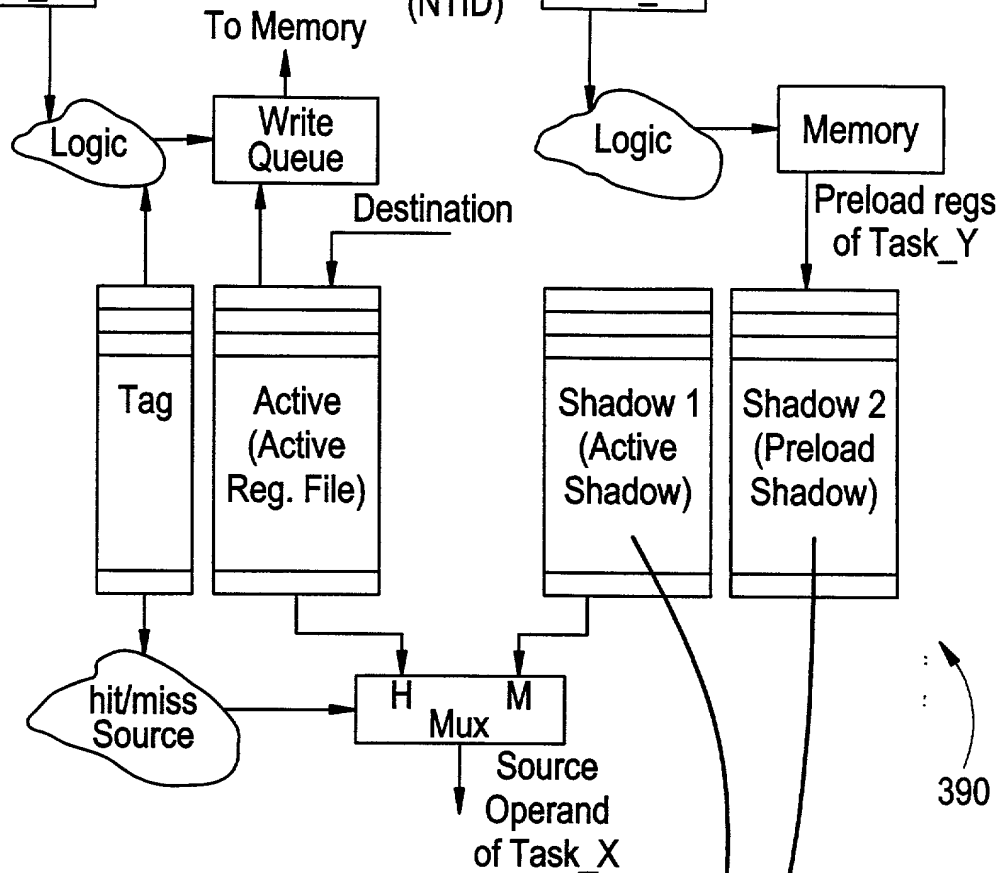


Current Task
(CTID)

Task_X

Next Task
(NTID)

Task_Y



After a task switch

Current Task
(CTID)

Task_Y

Next Task
(NTID)

Task_Z

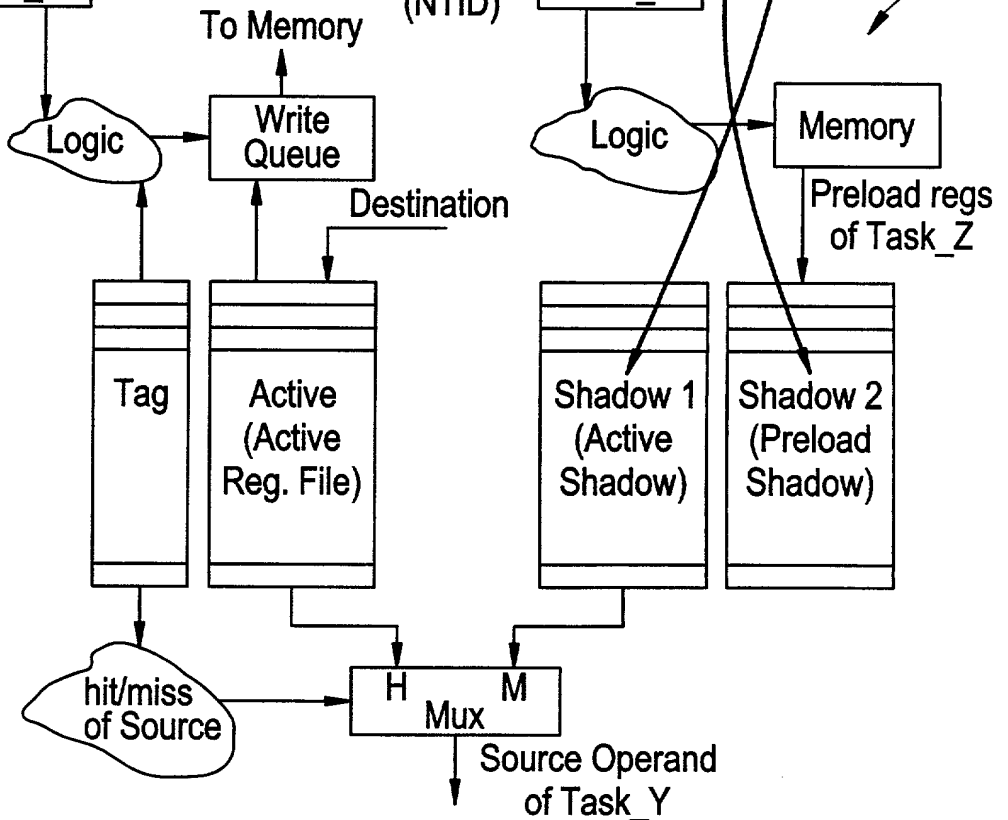


FIG. 39

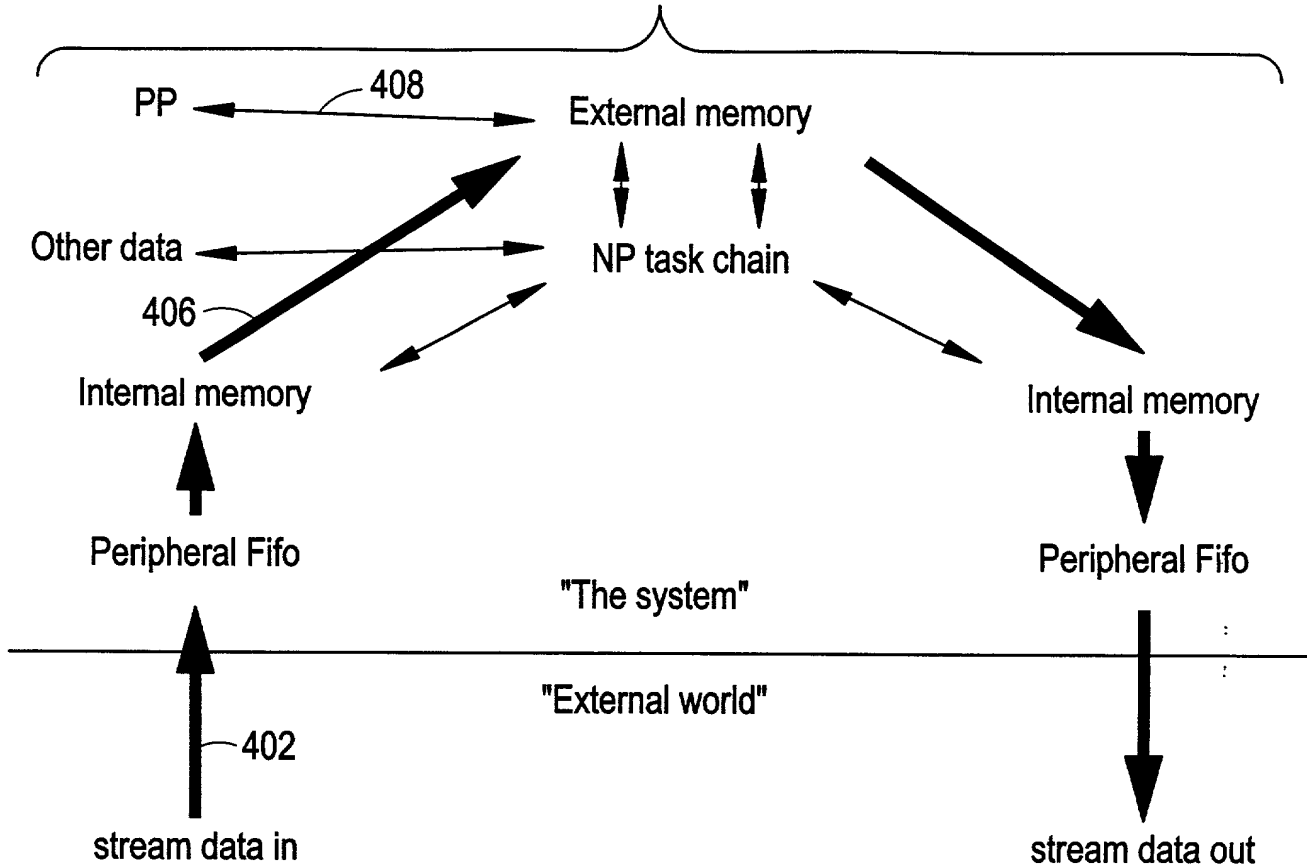


FIG. 40

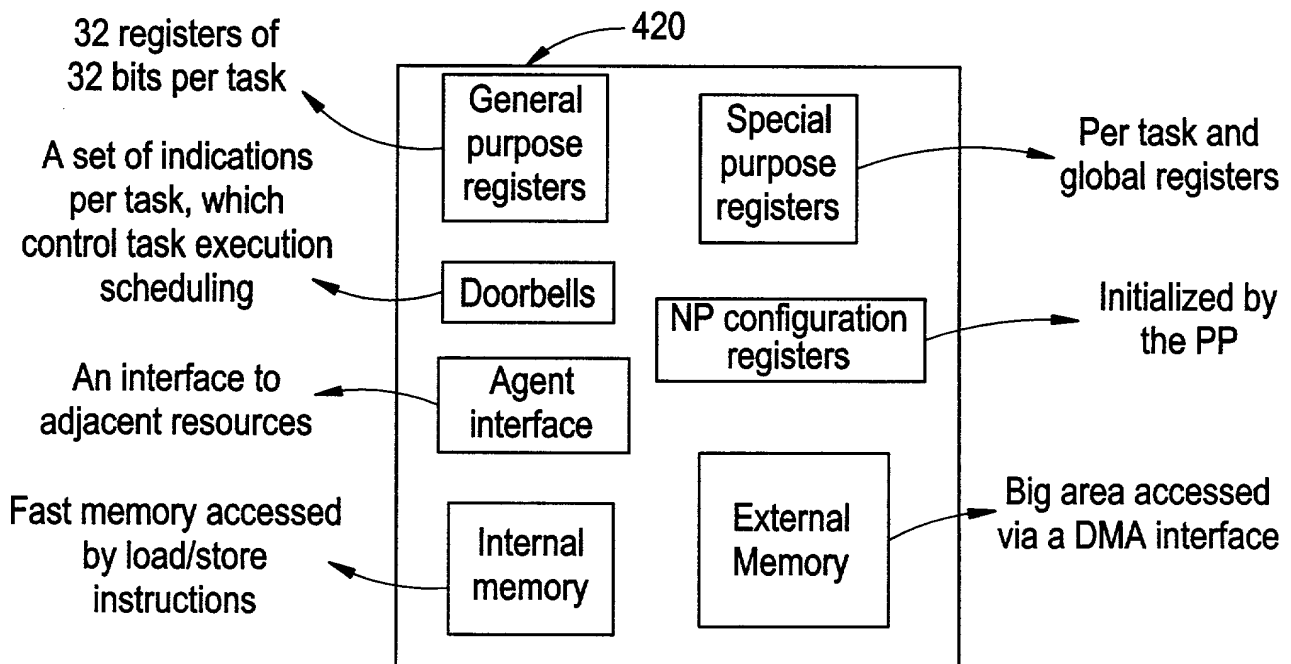
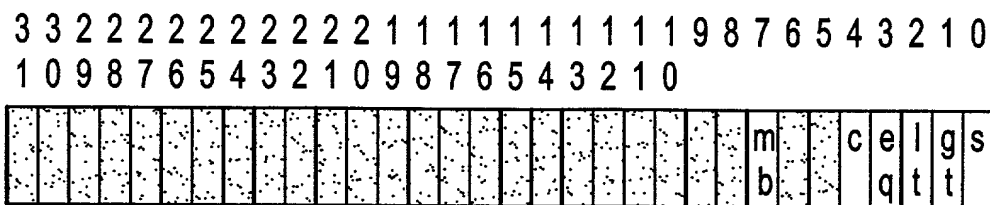




FIG. 41

R1 register



s - sticky bit

eq - equal/zero

It - less than/negative

gt - greater then/positive

c - carry

mb - reflection of the RAM mult-reader busy indication

FIG. 42

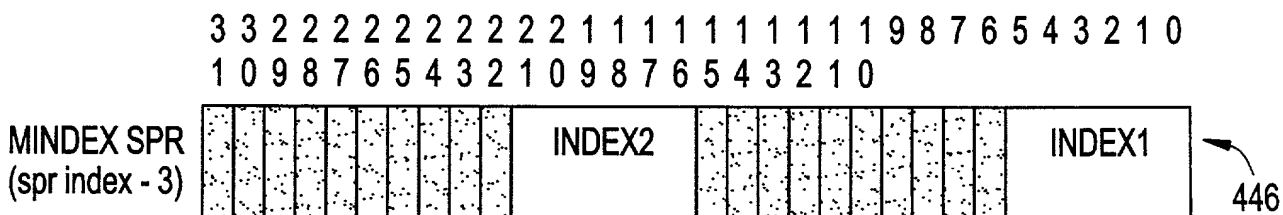
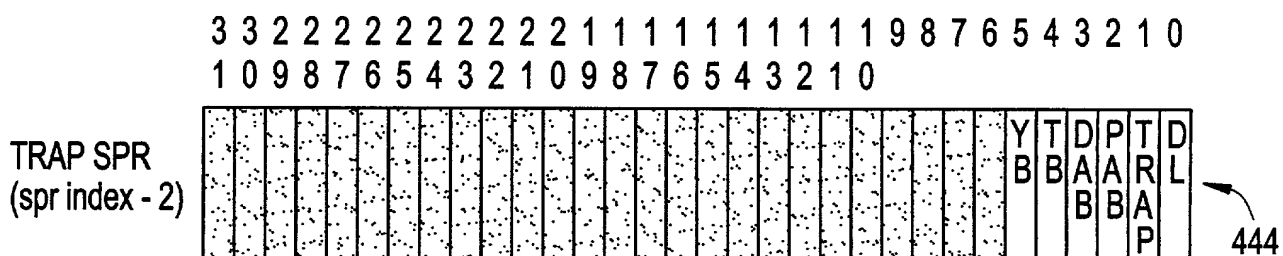
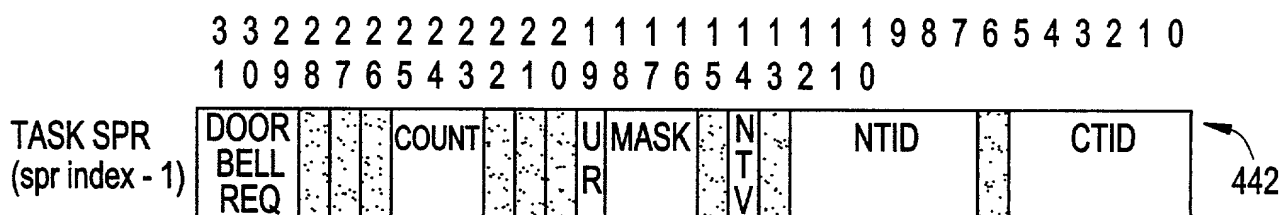
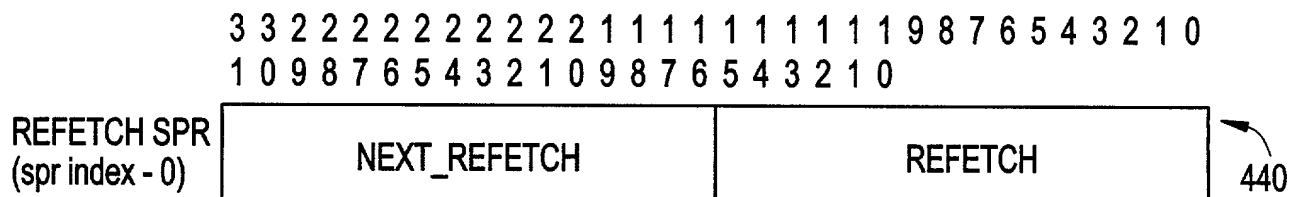


FIG. 43

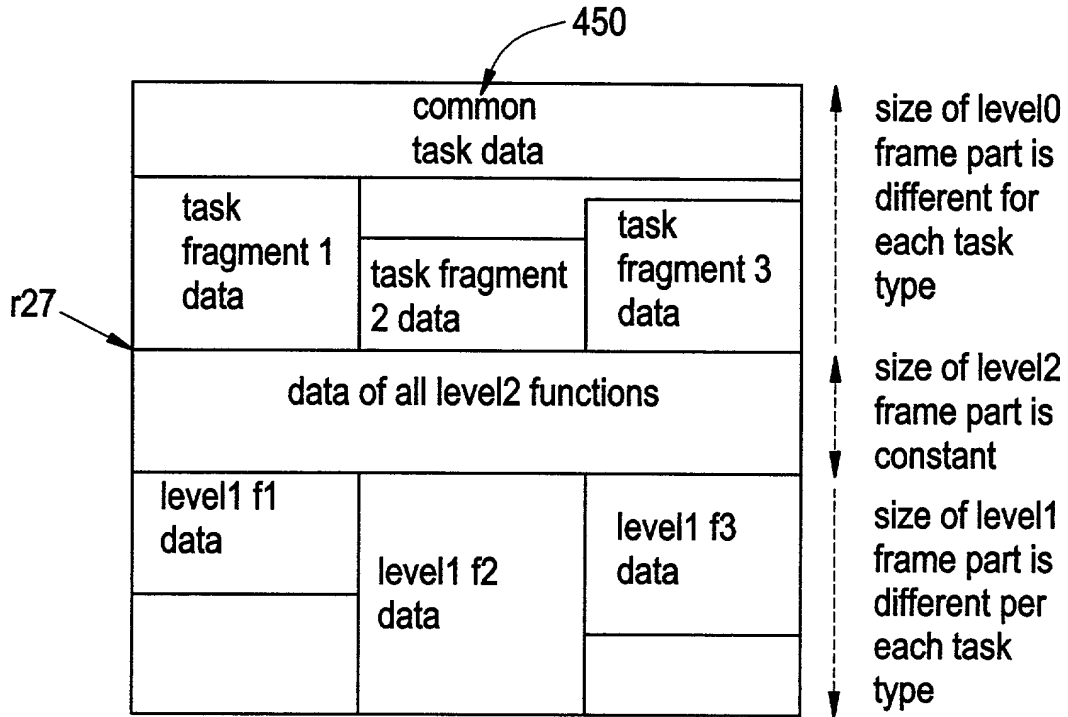


FIG. 44

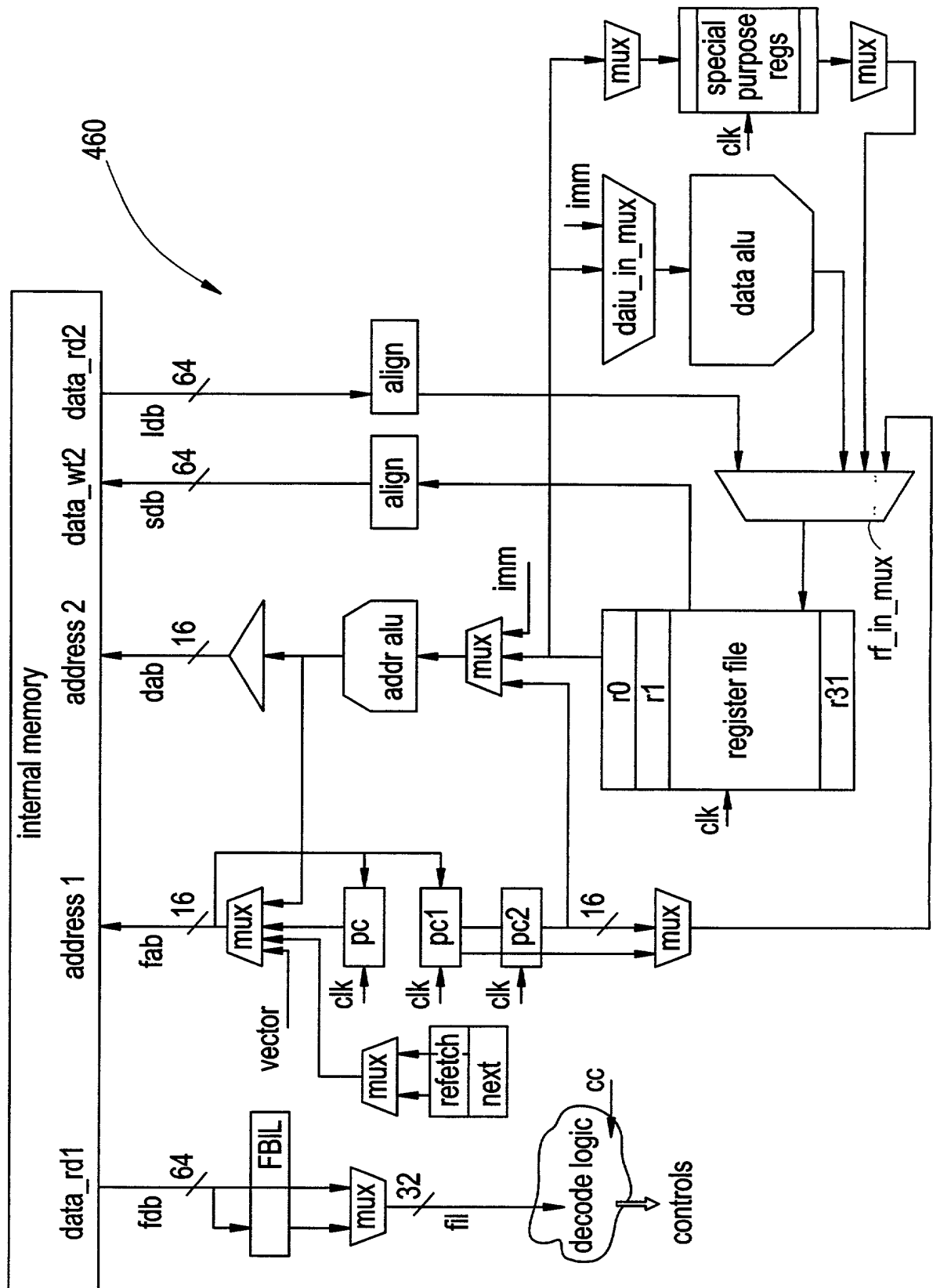


FIG. 45

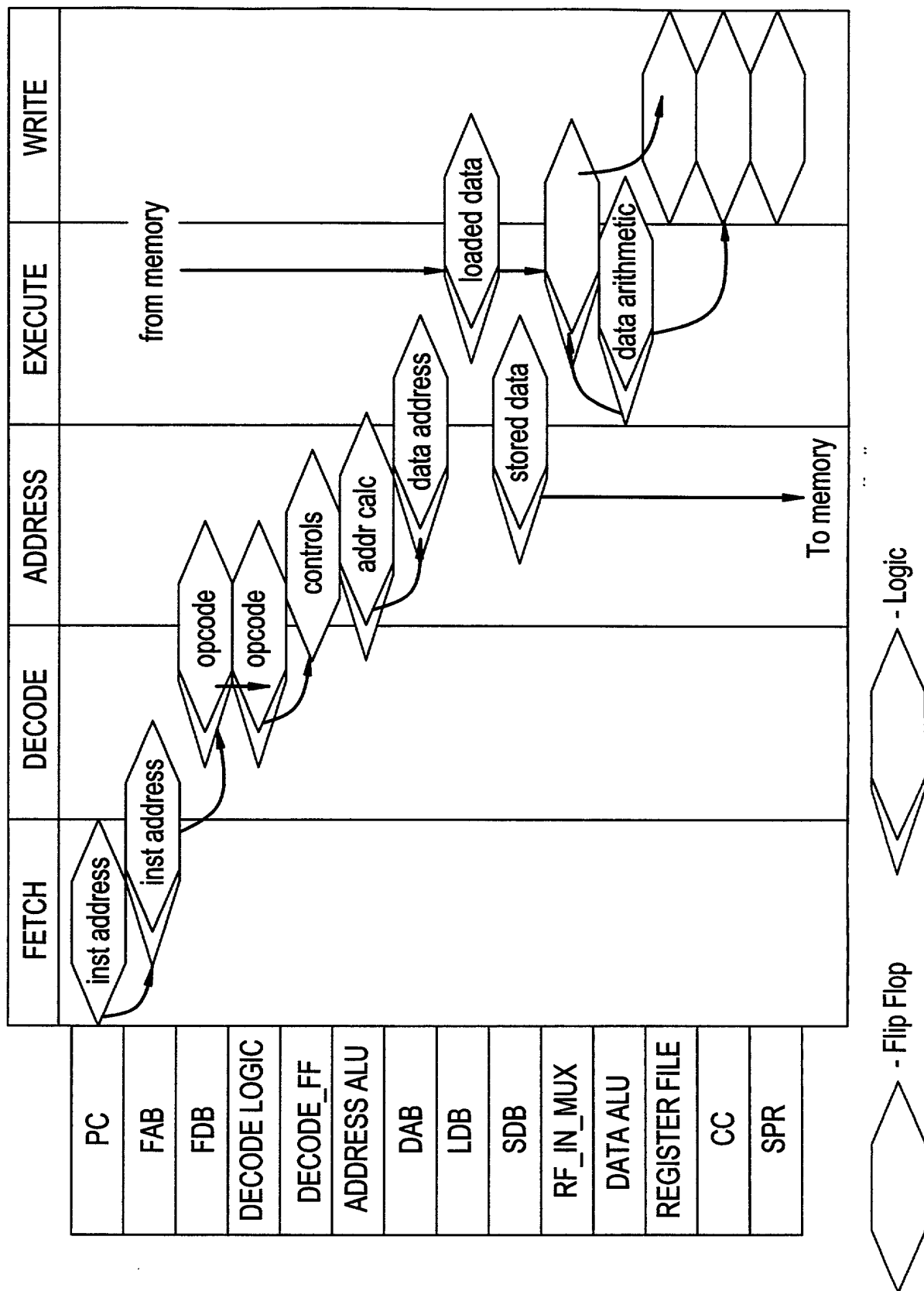


FIG. 46

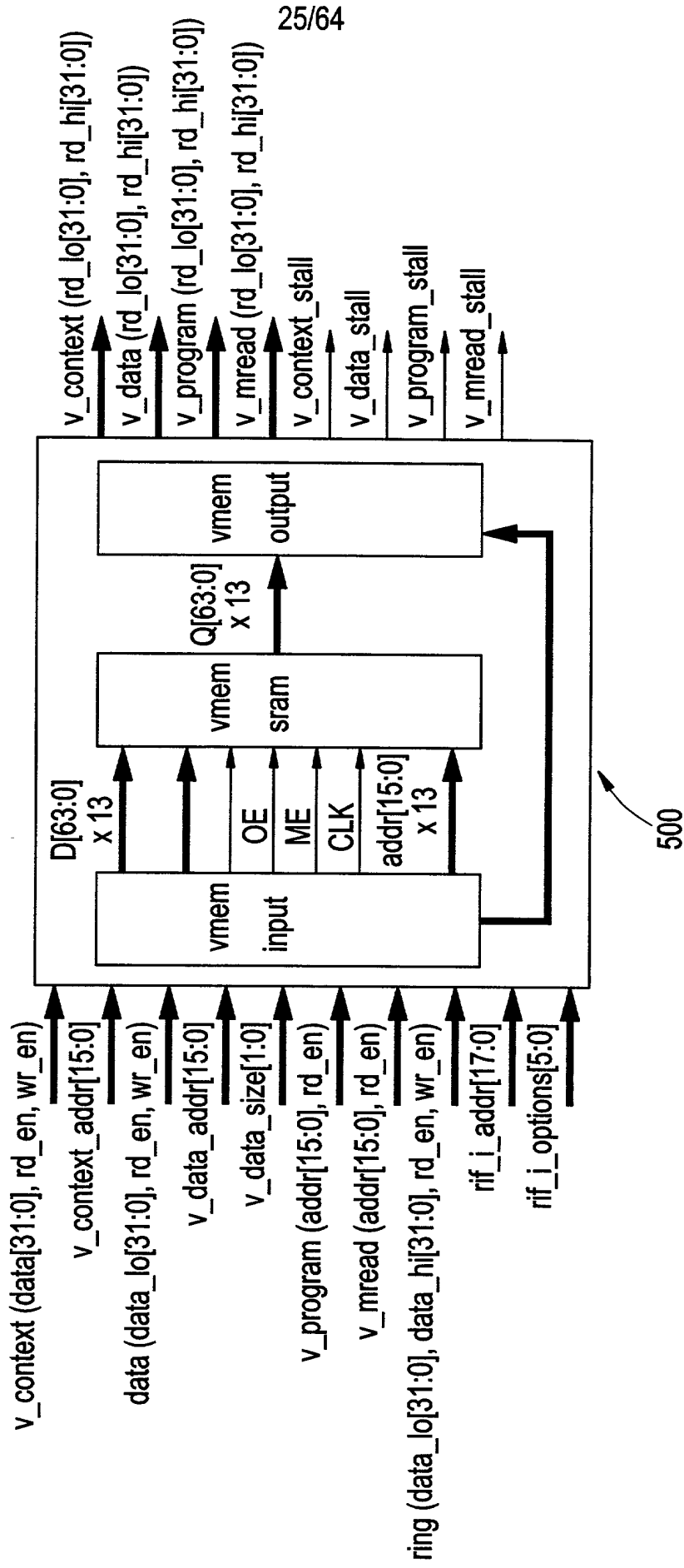
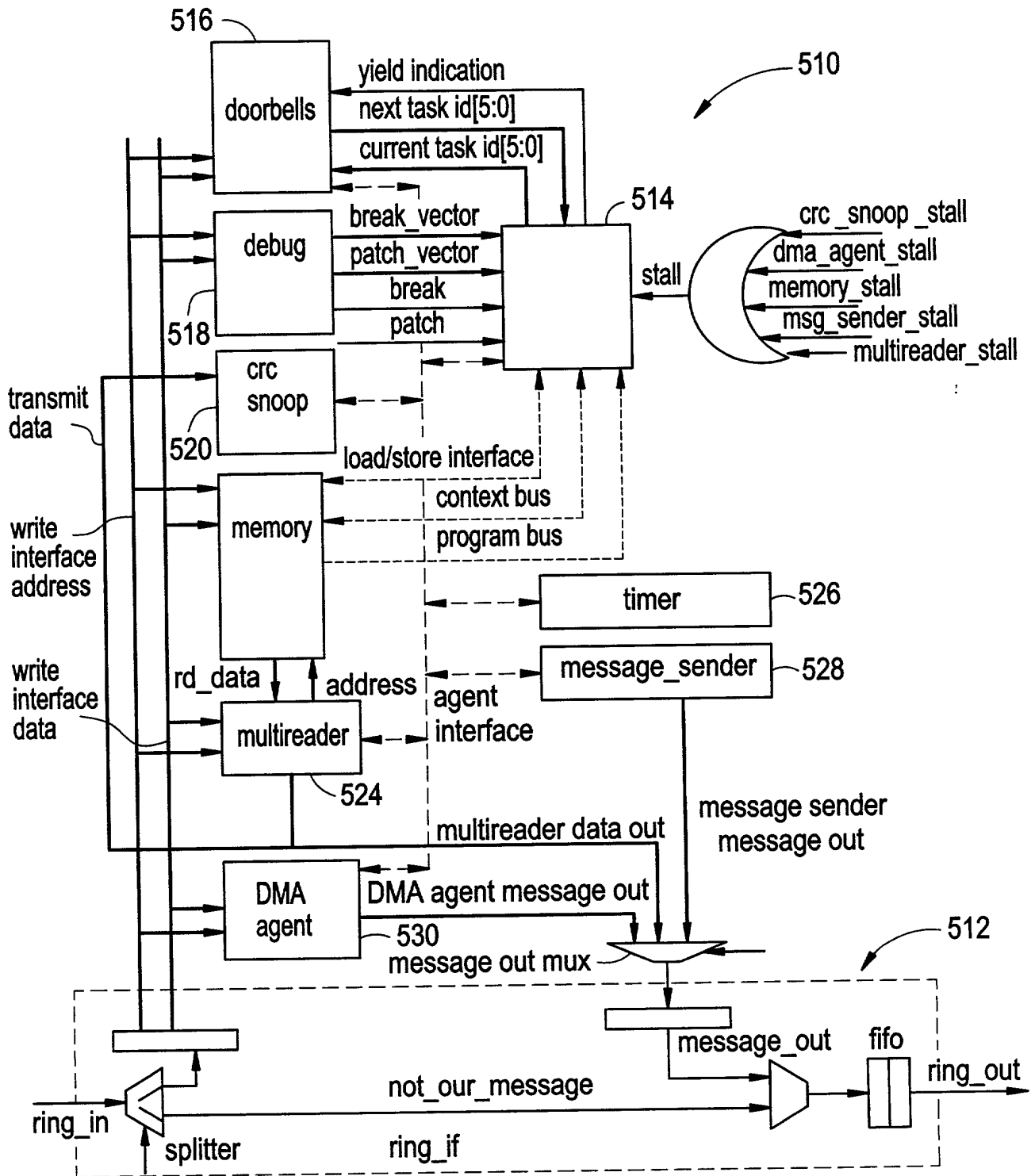


FIG. 47



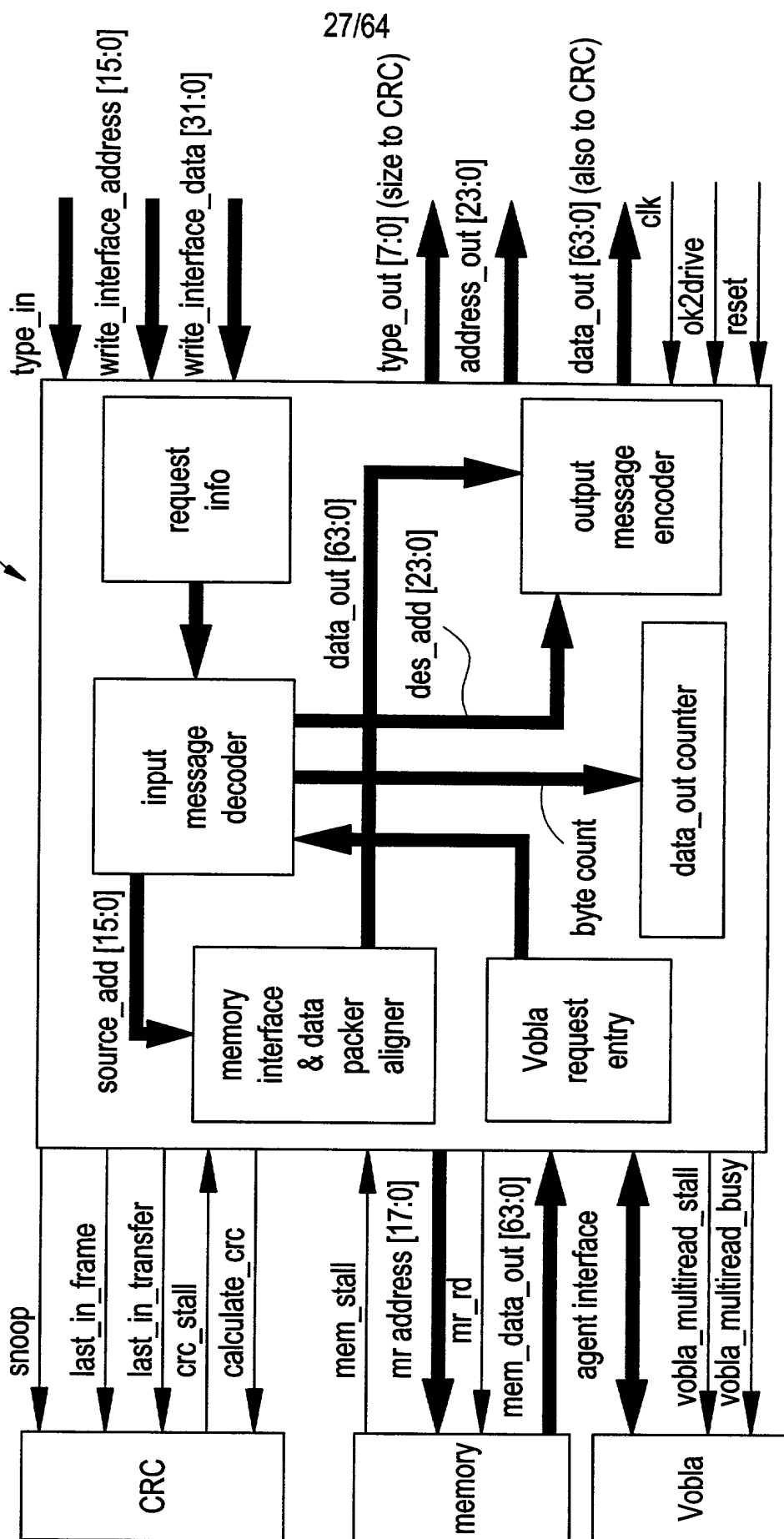


FIG. 49

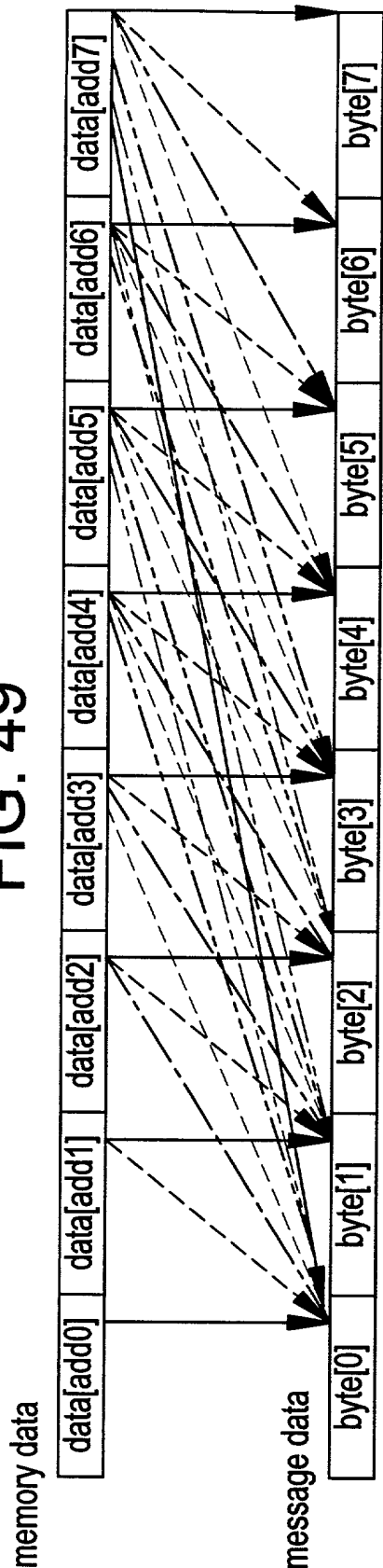


FIG. 50

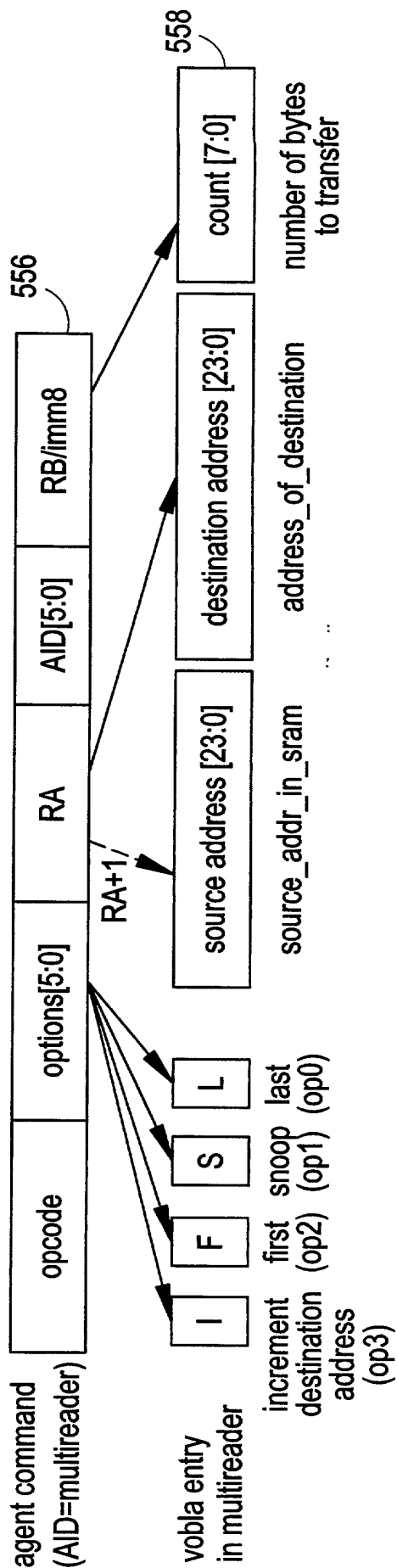


FIG. 51

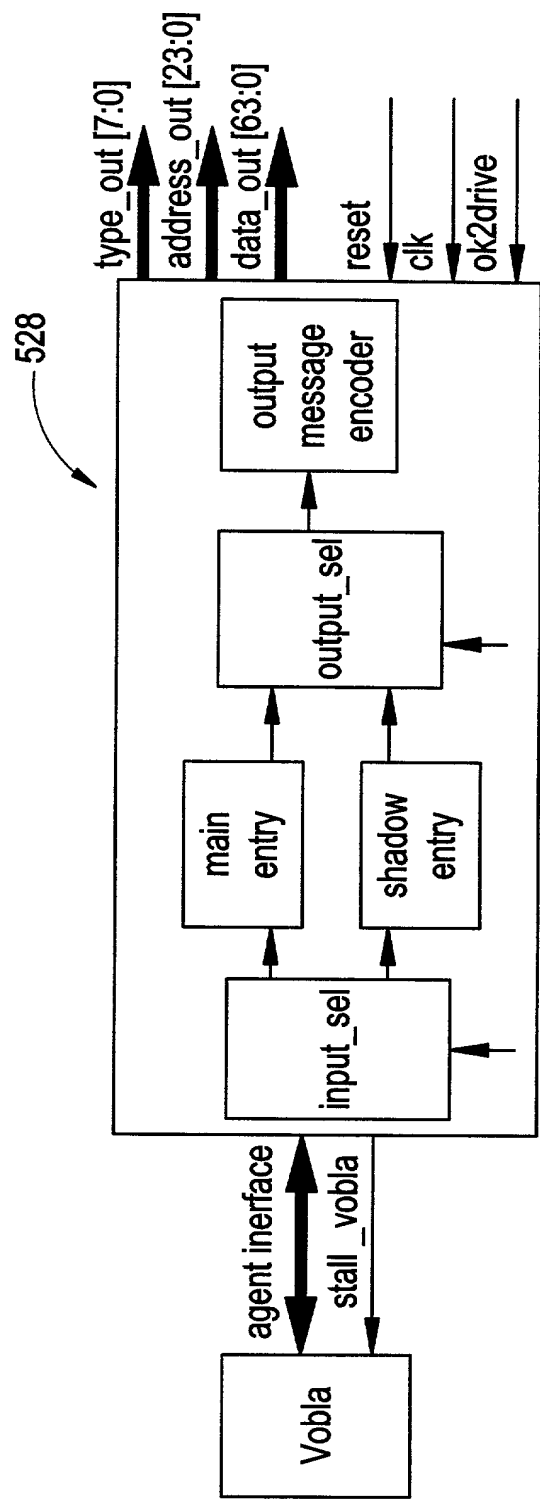


FIG. 52

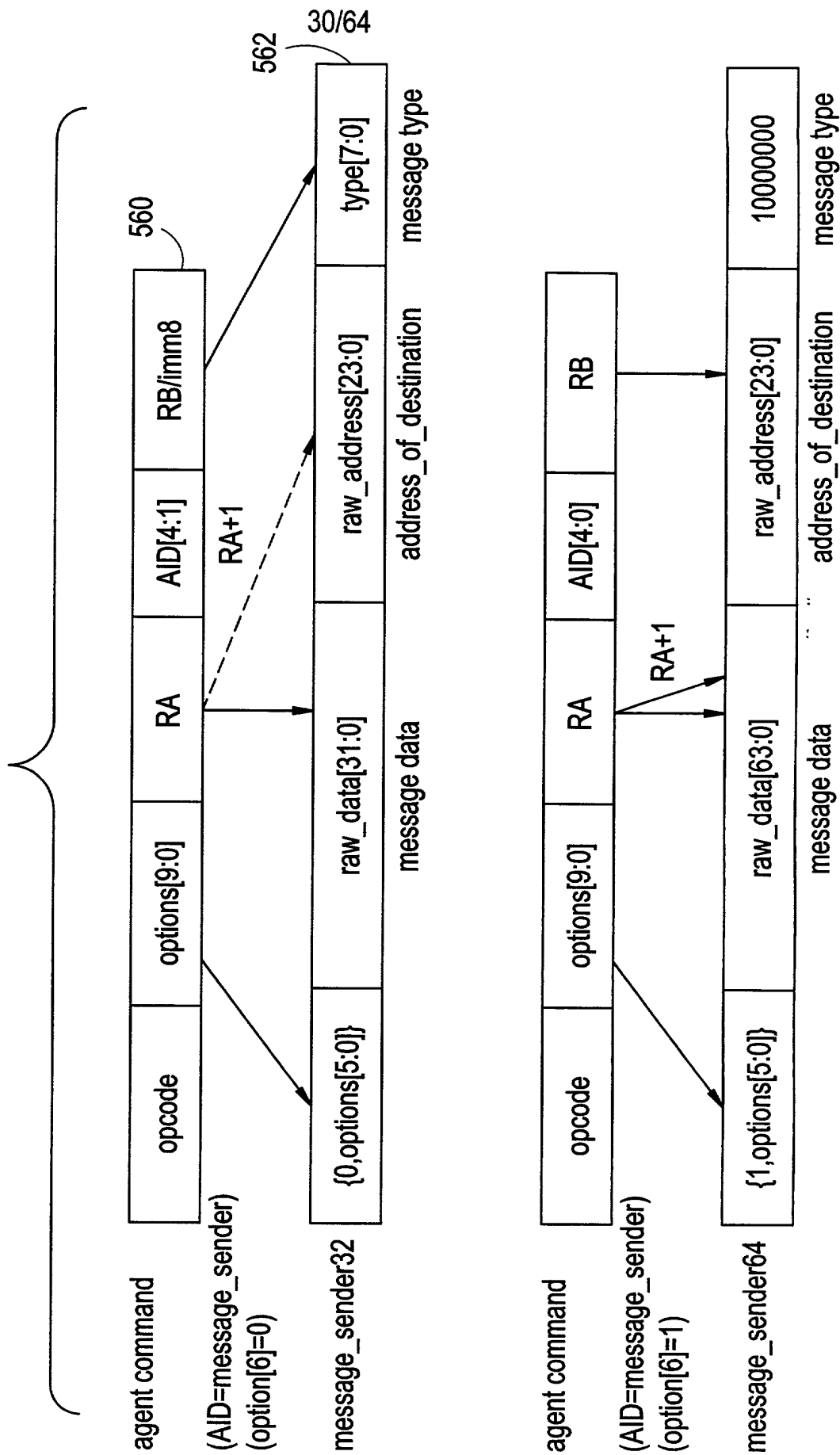
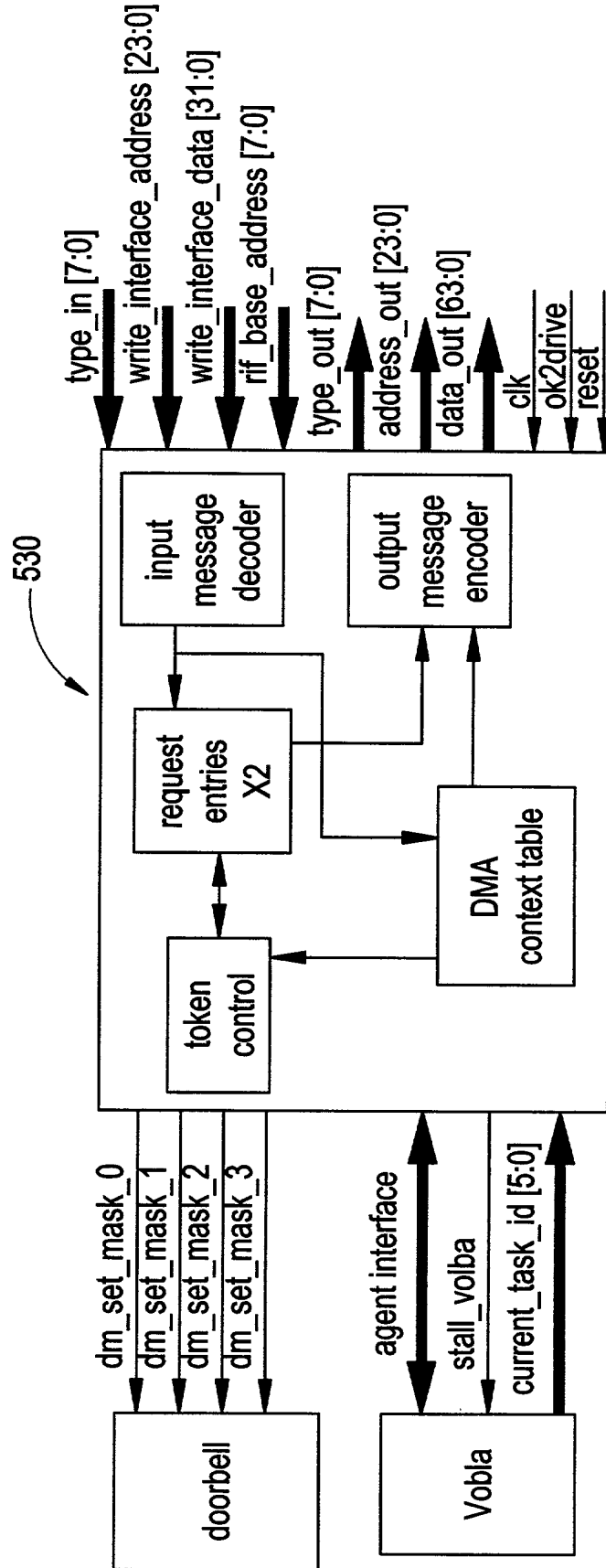


FIG. 53



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FIG. 54

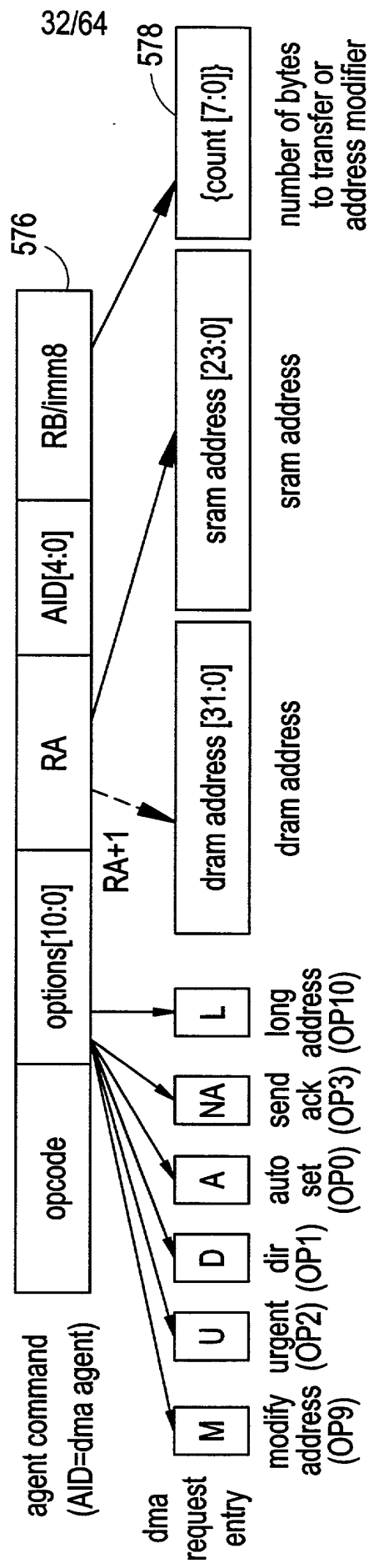




FIG. 55

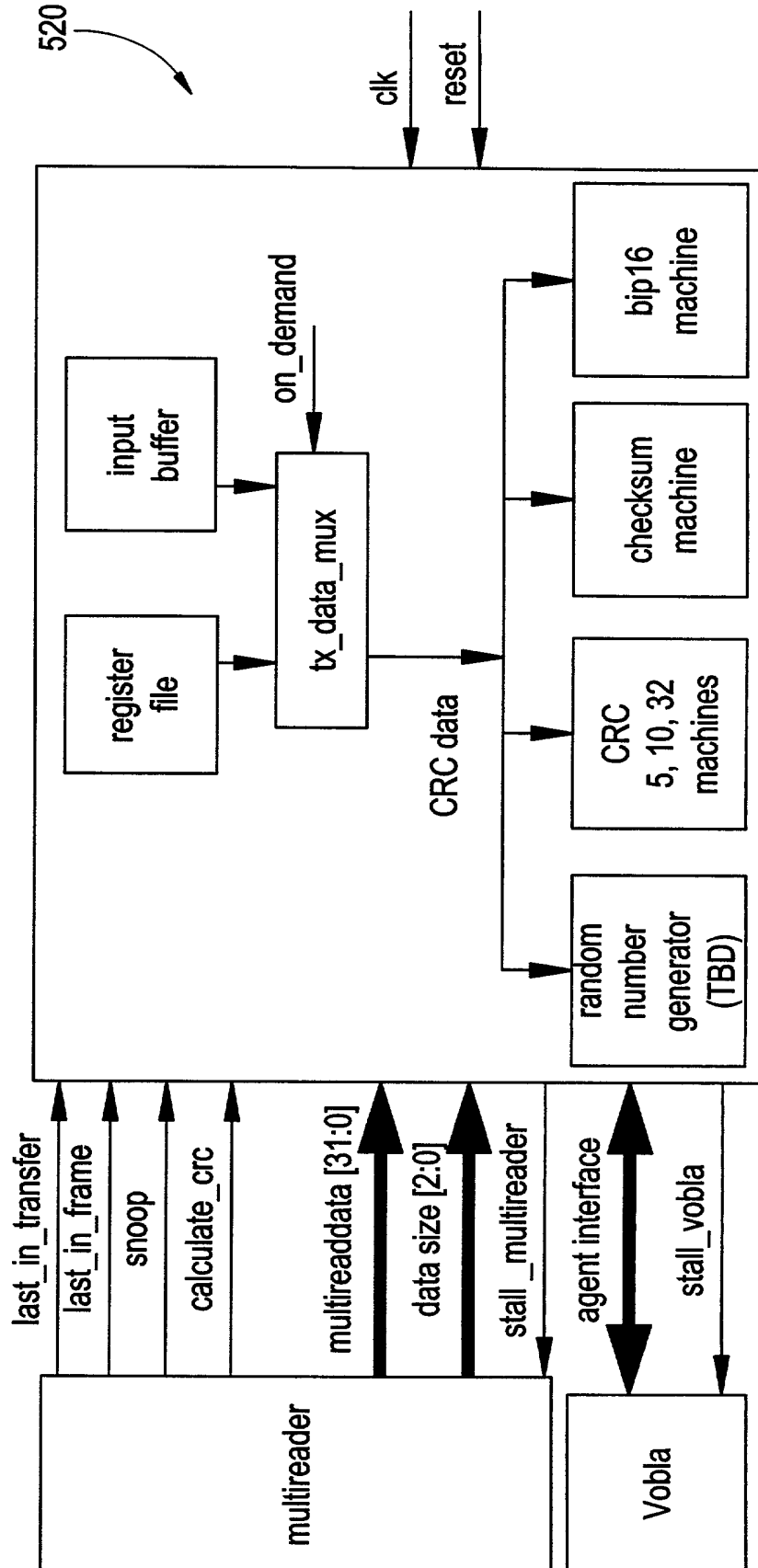


FIG. 56

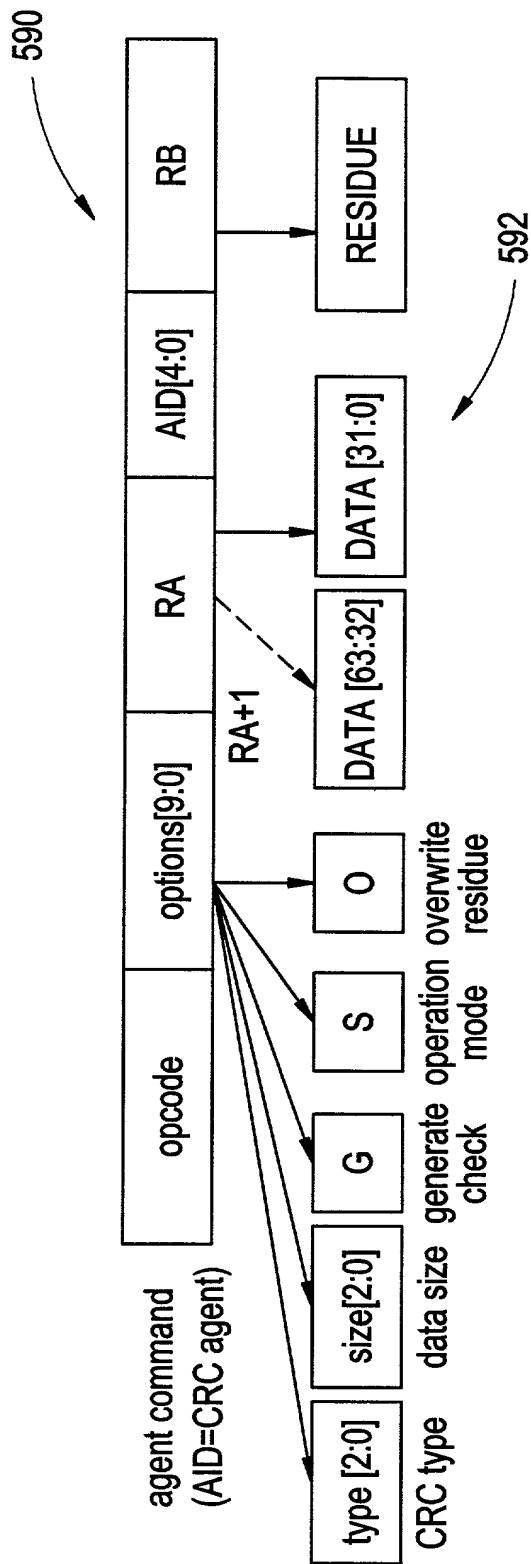


FIG. 57

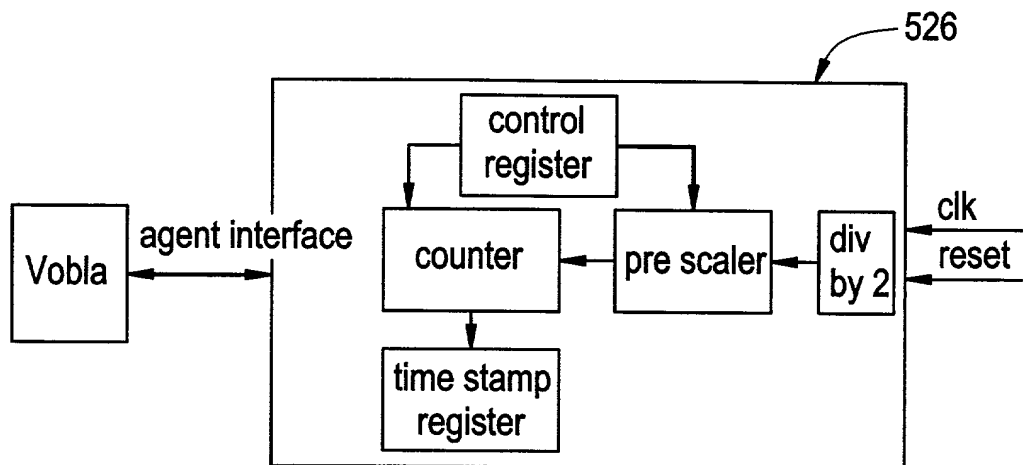
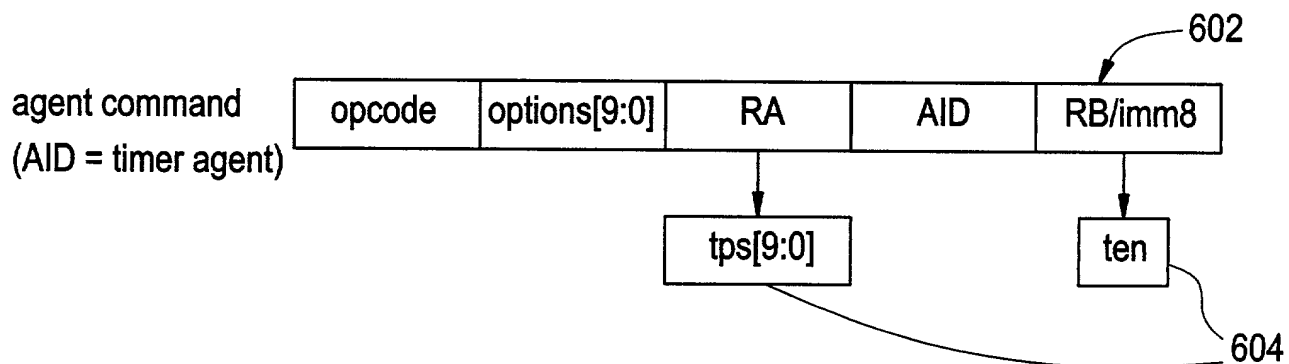
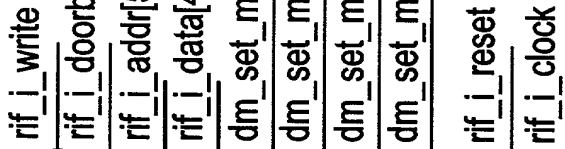


FIG. 58



516



-612

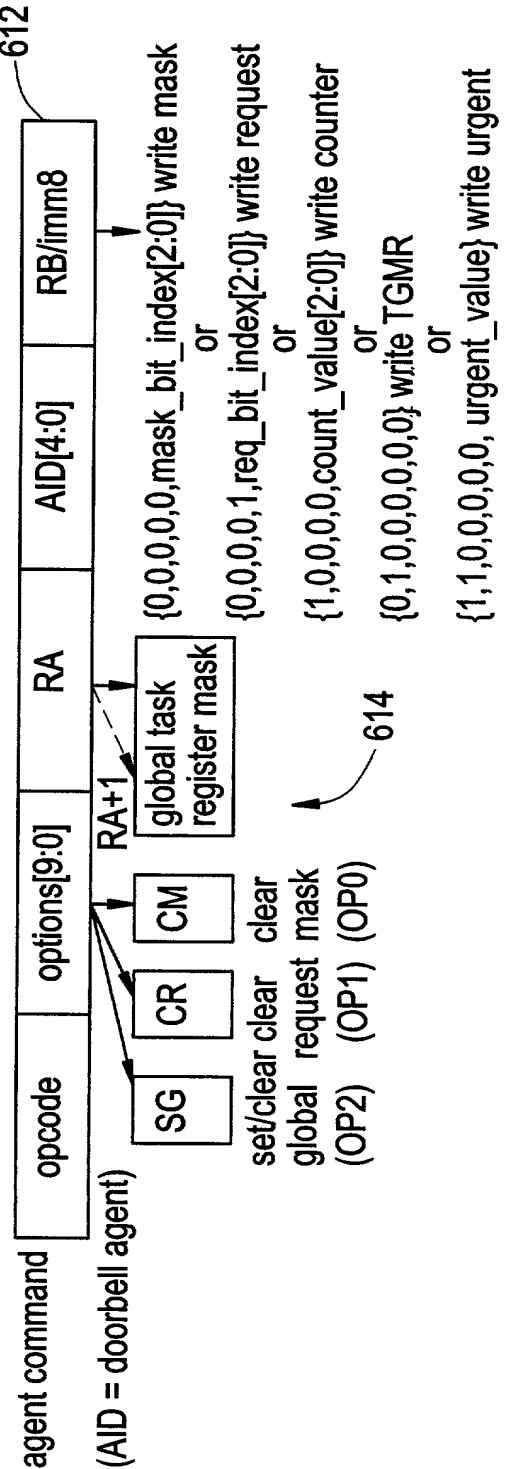


FIG. 61

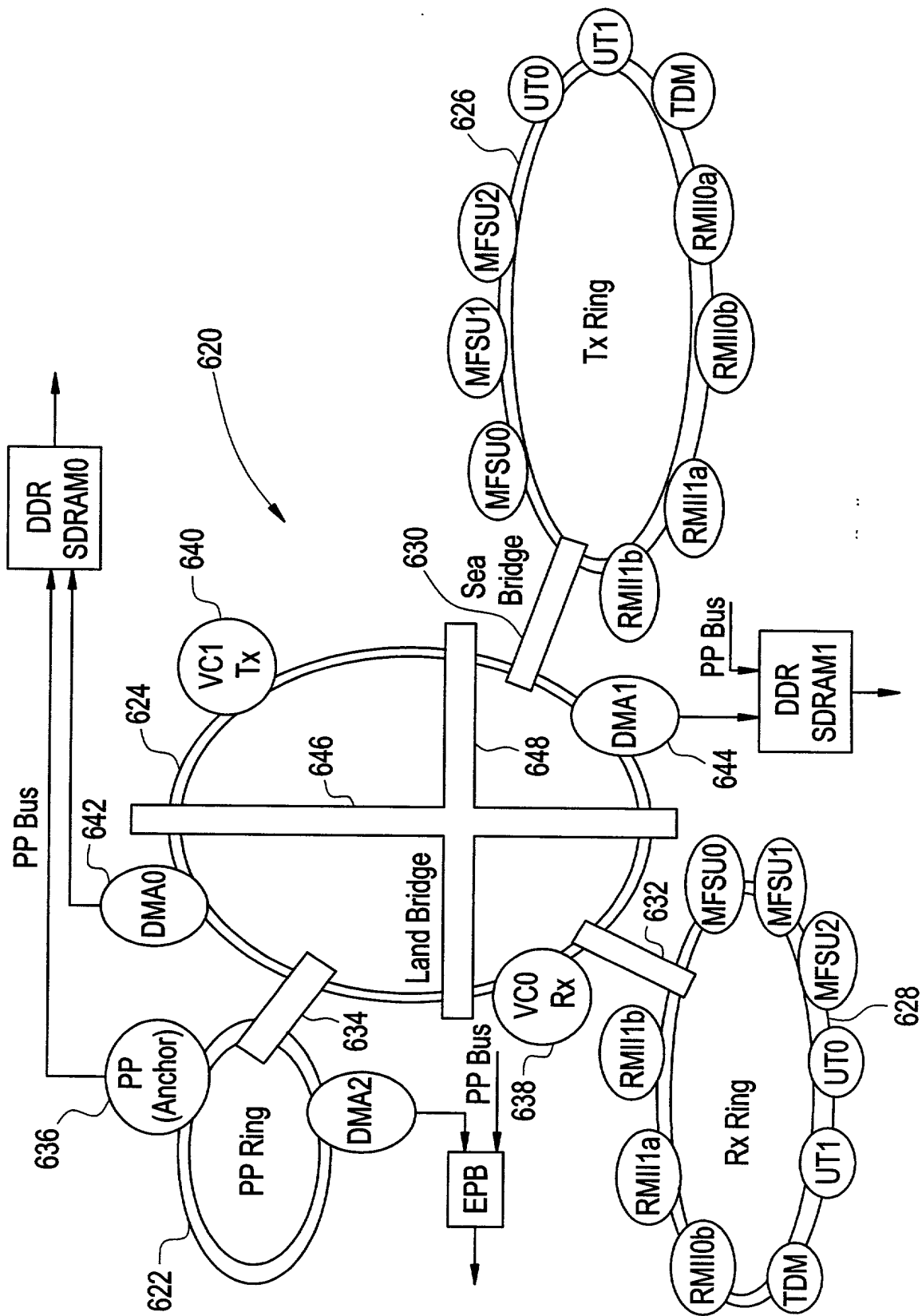


FIG. 62

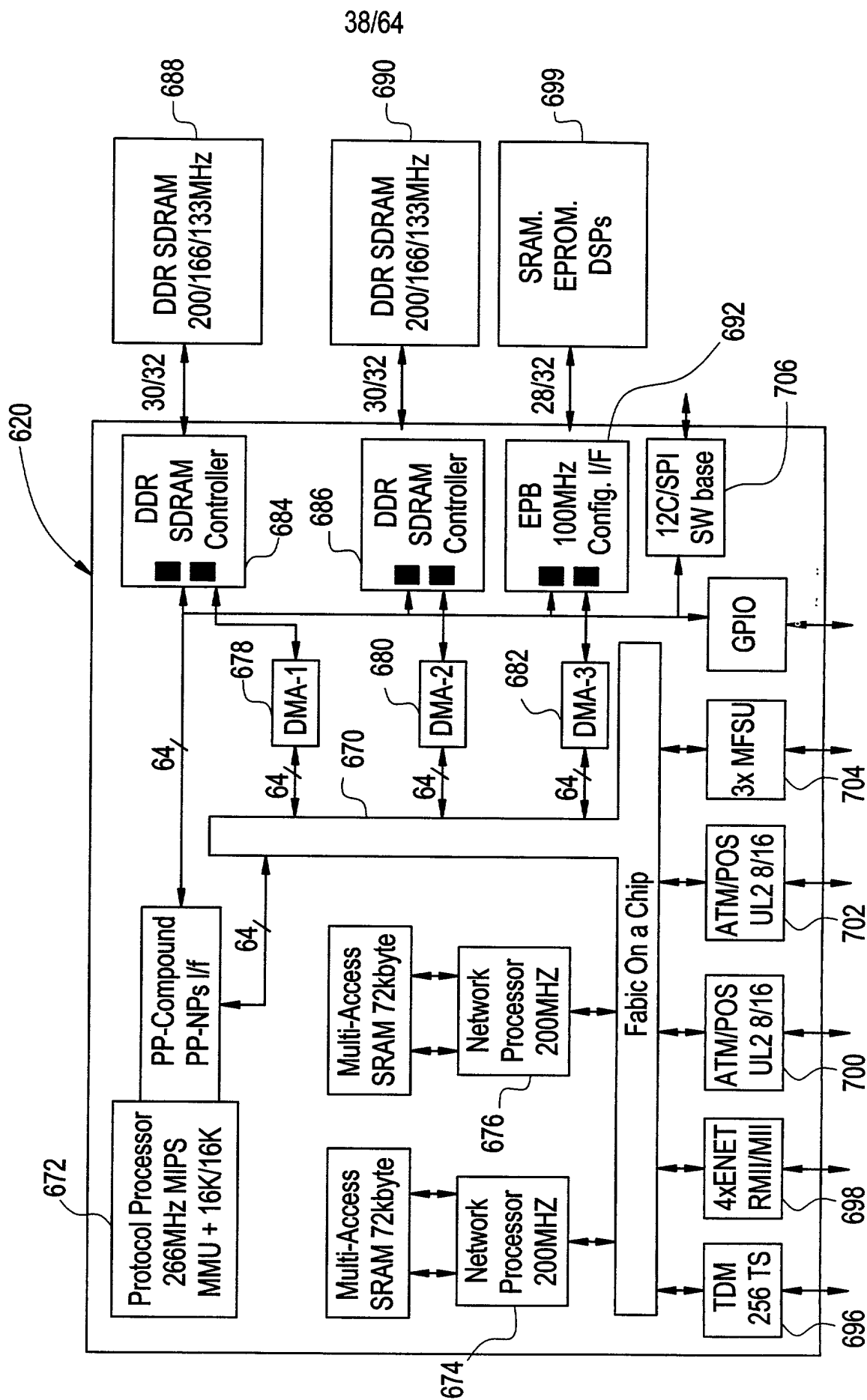


FIG. 63

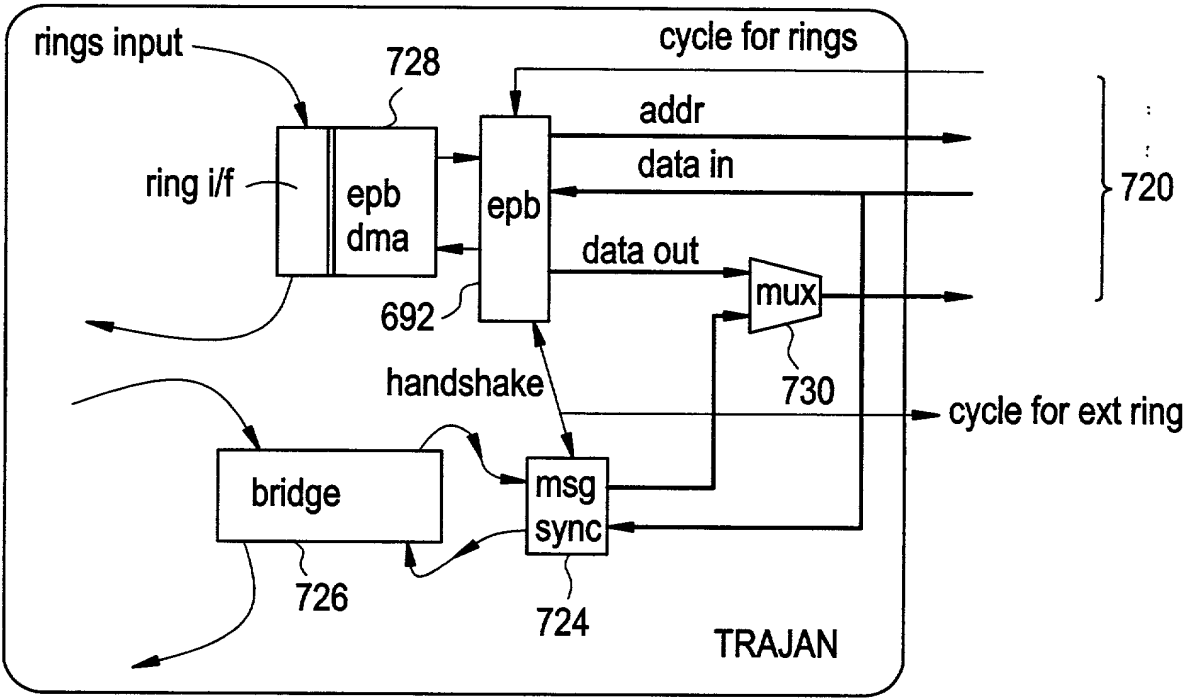
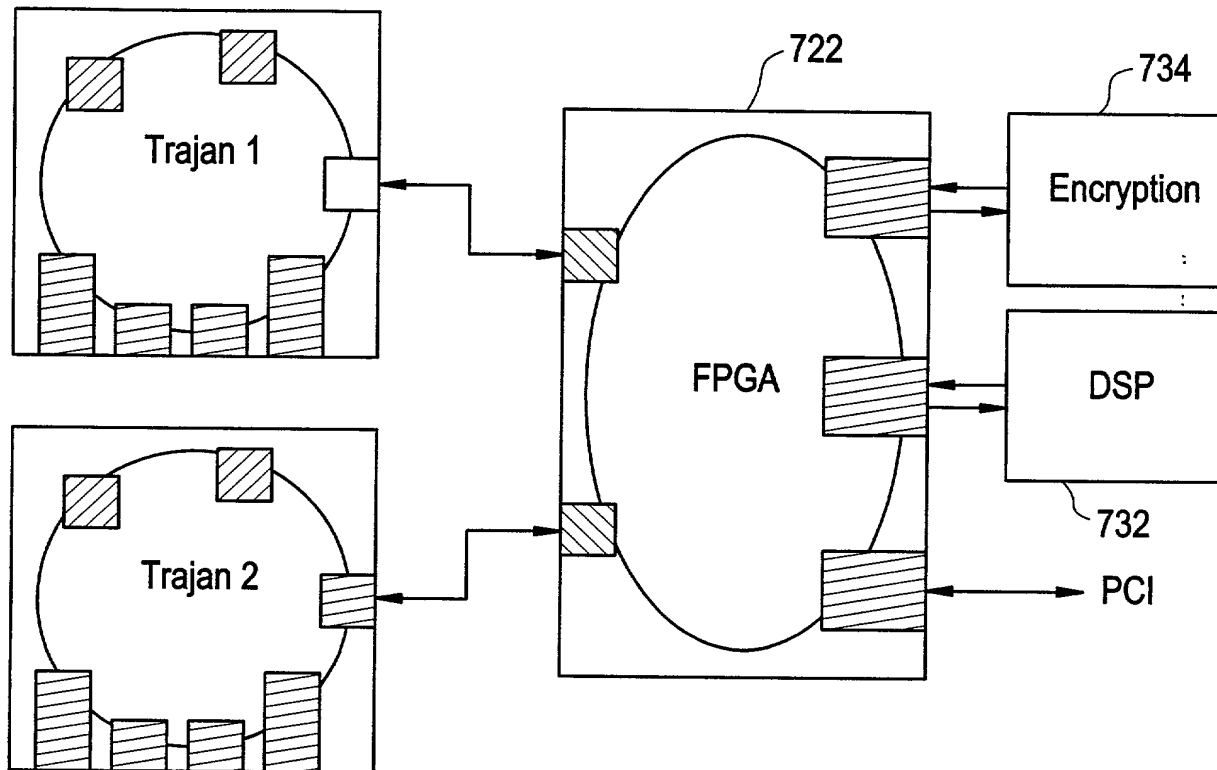


FIG. 64



- Processor
- Functional unit
- External ring interface

FIG. 65

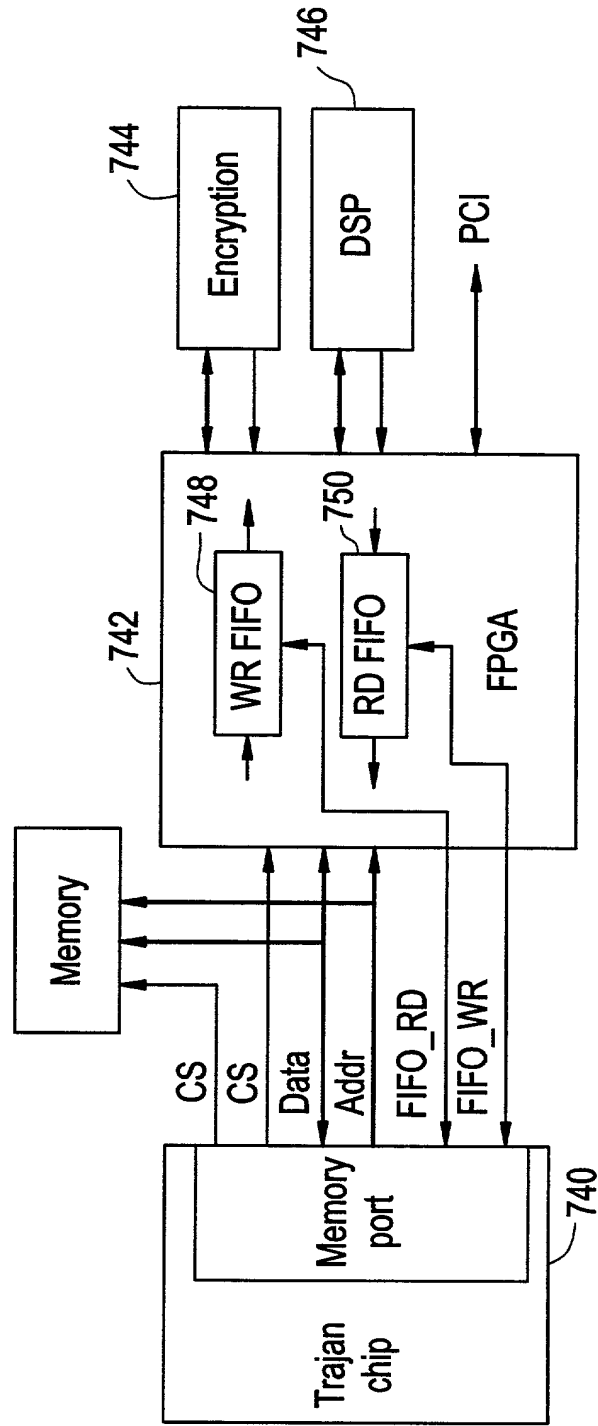


FIG. 66

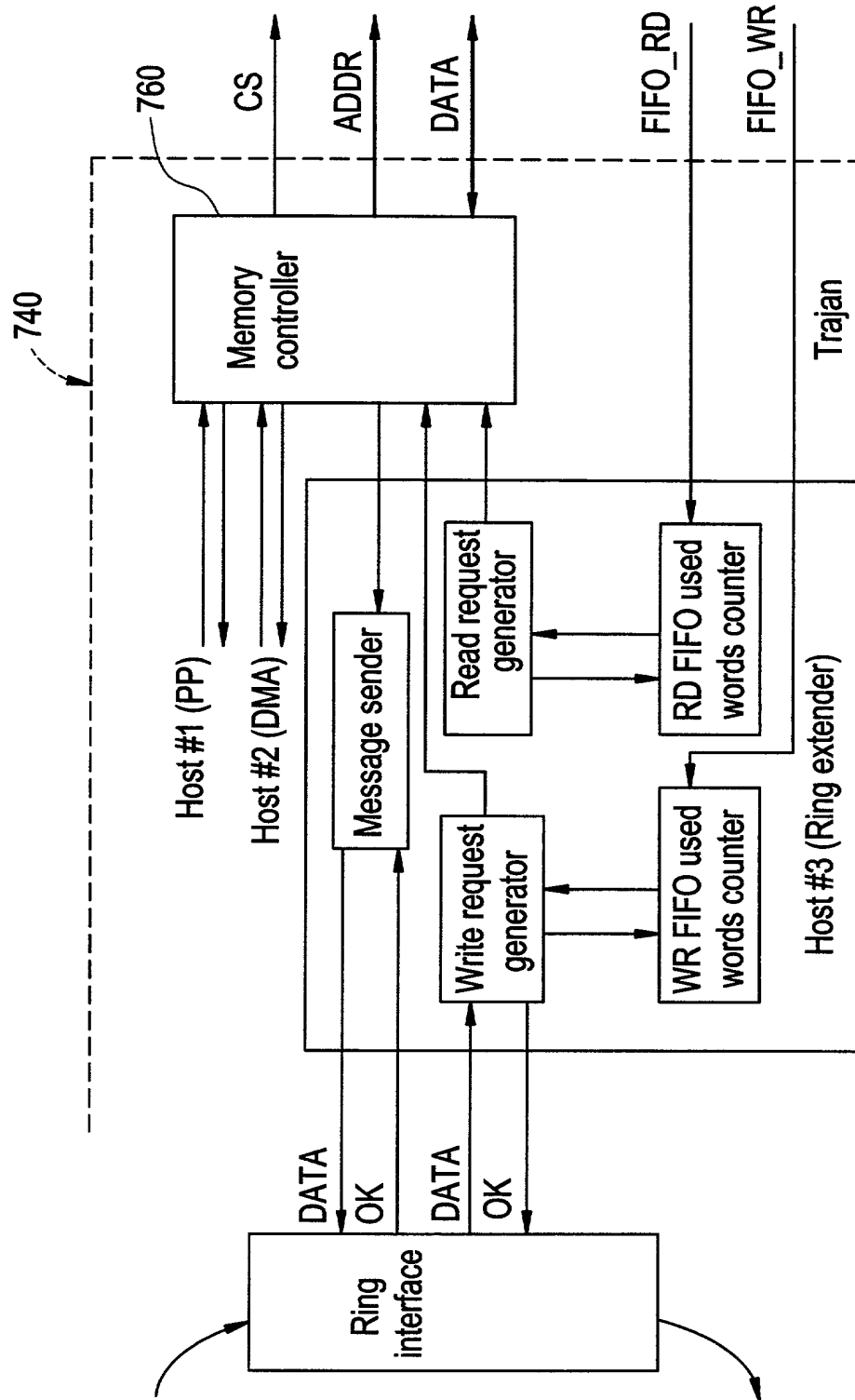


FIG. 67

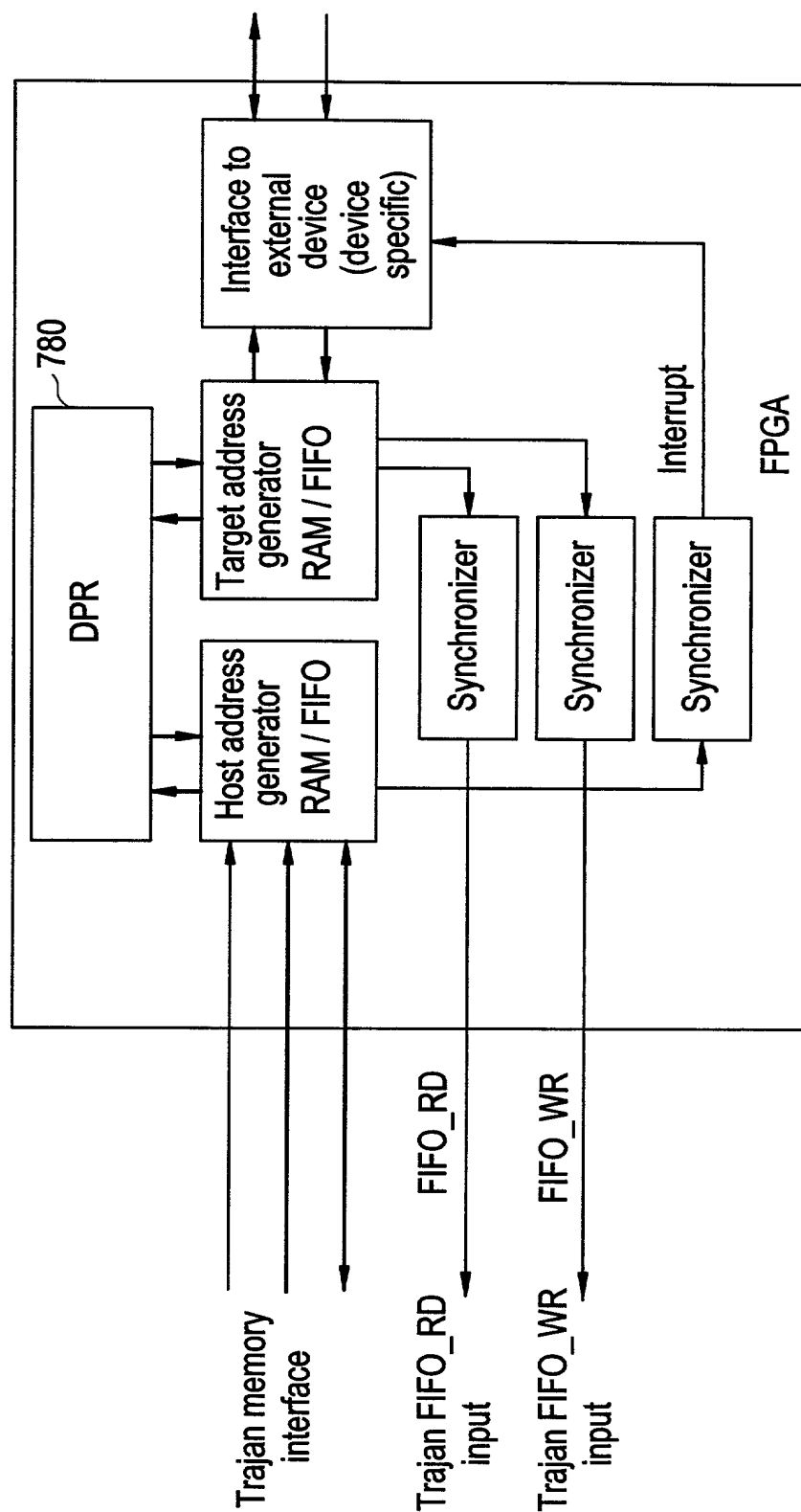


FIG. 68

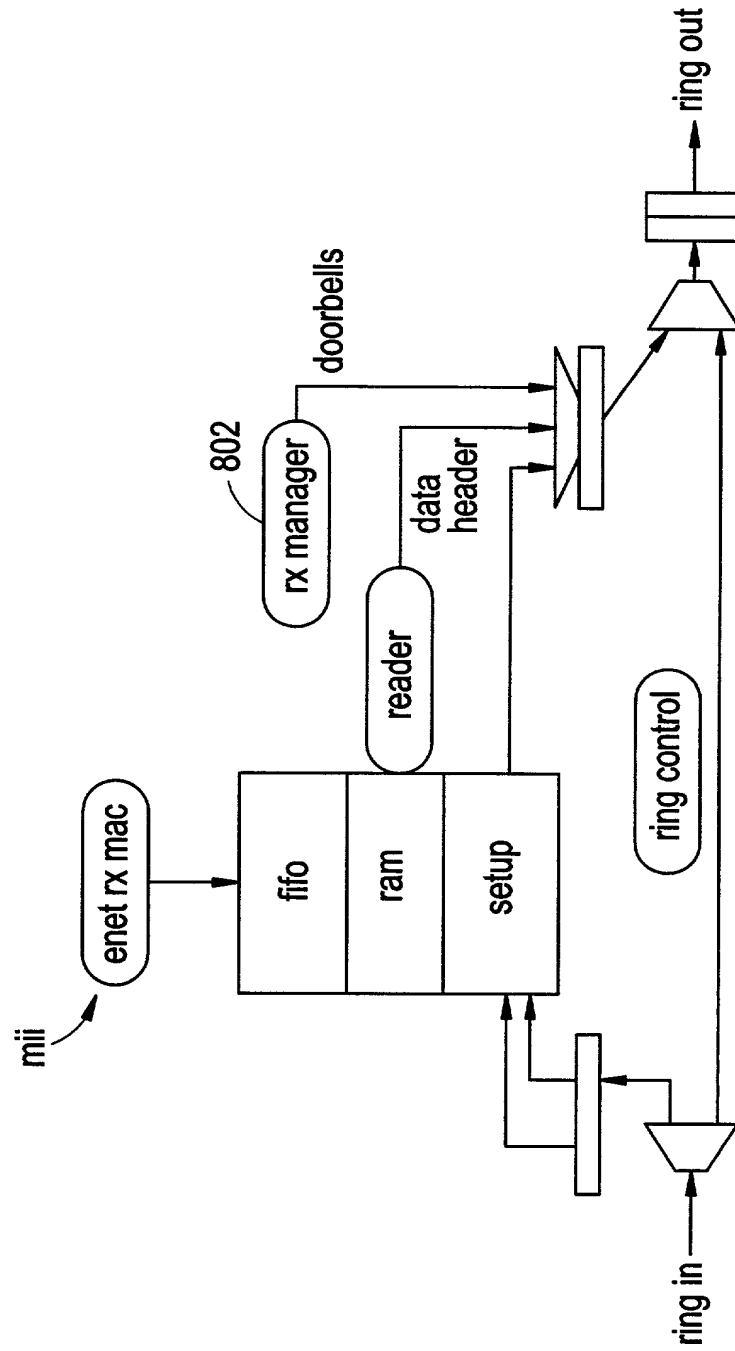


FIG. 69

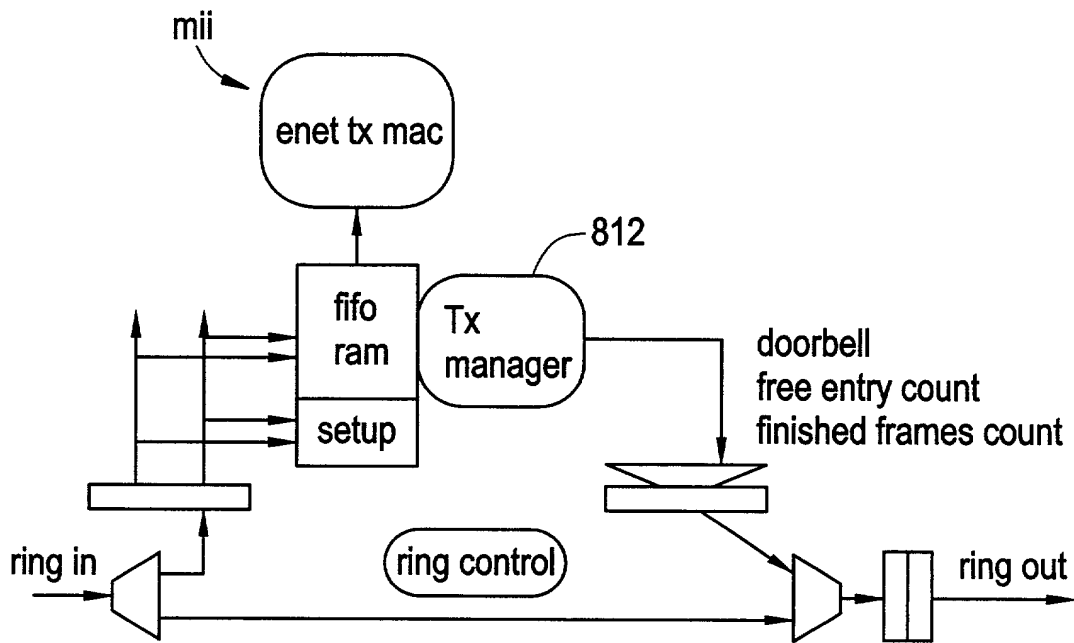




FIG. 70

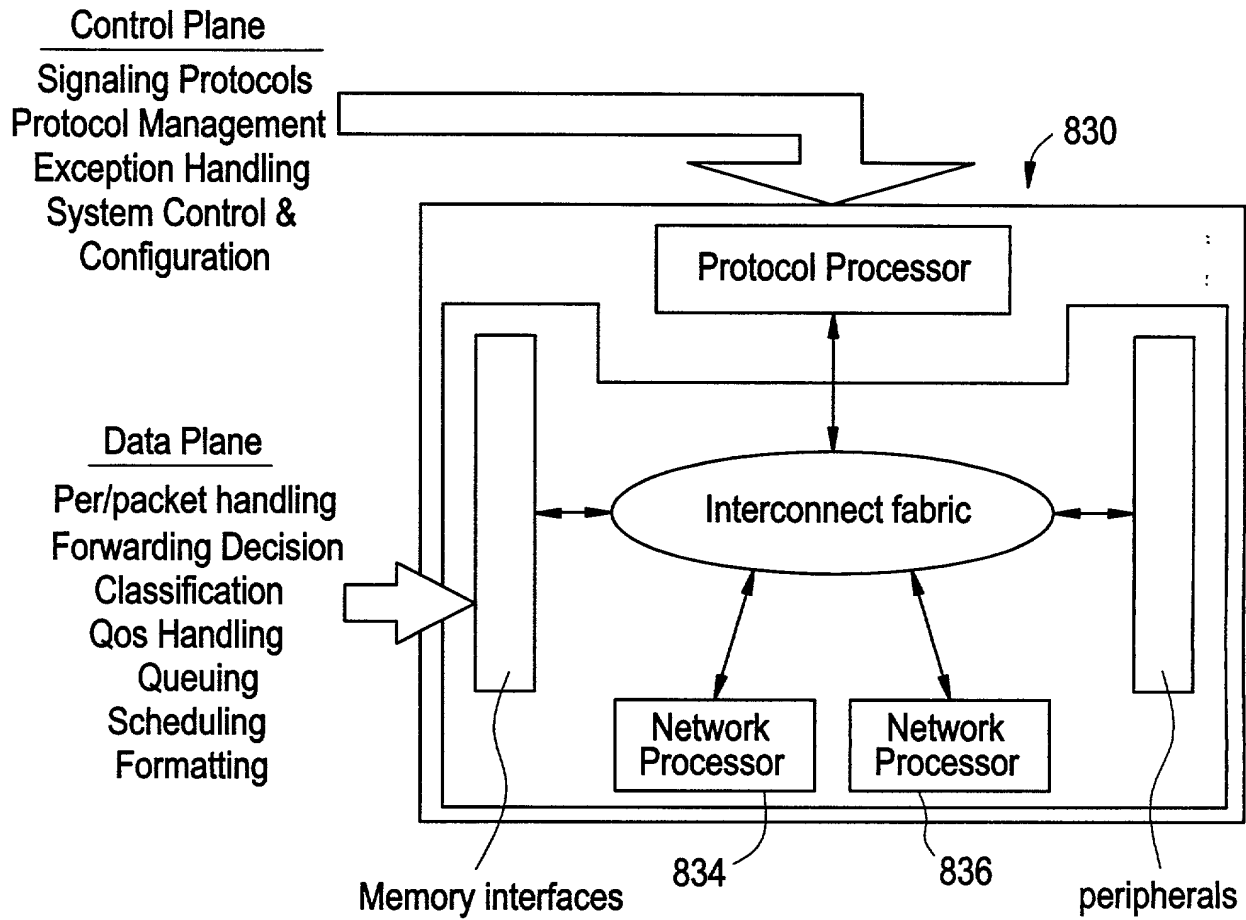


FIG. 71

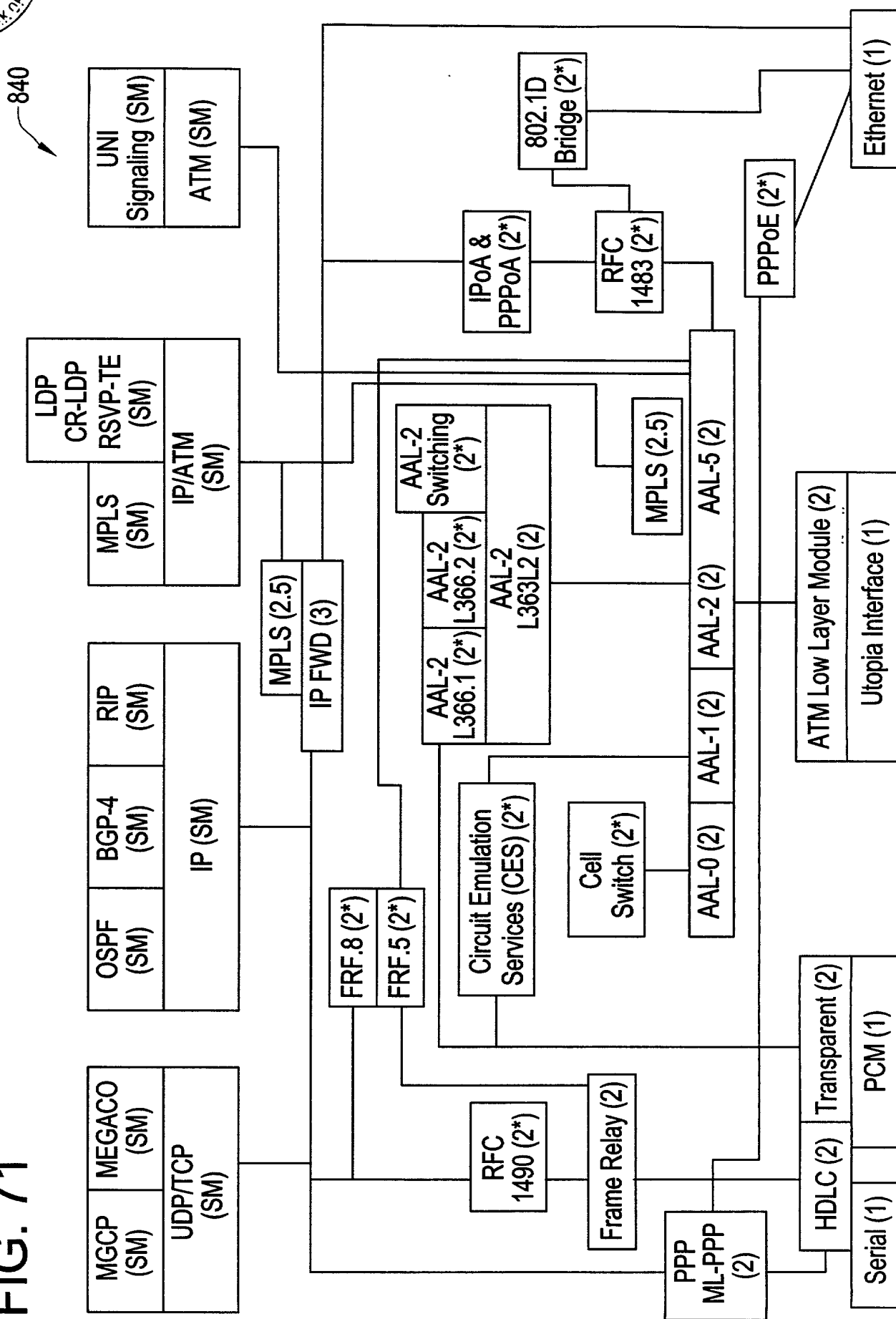
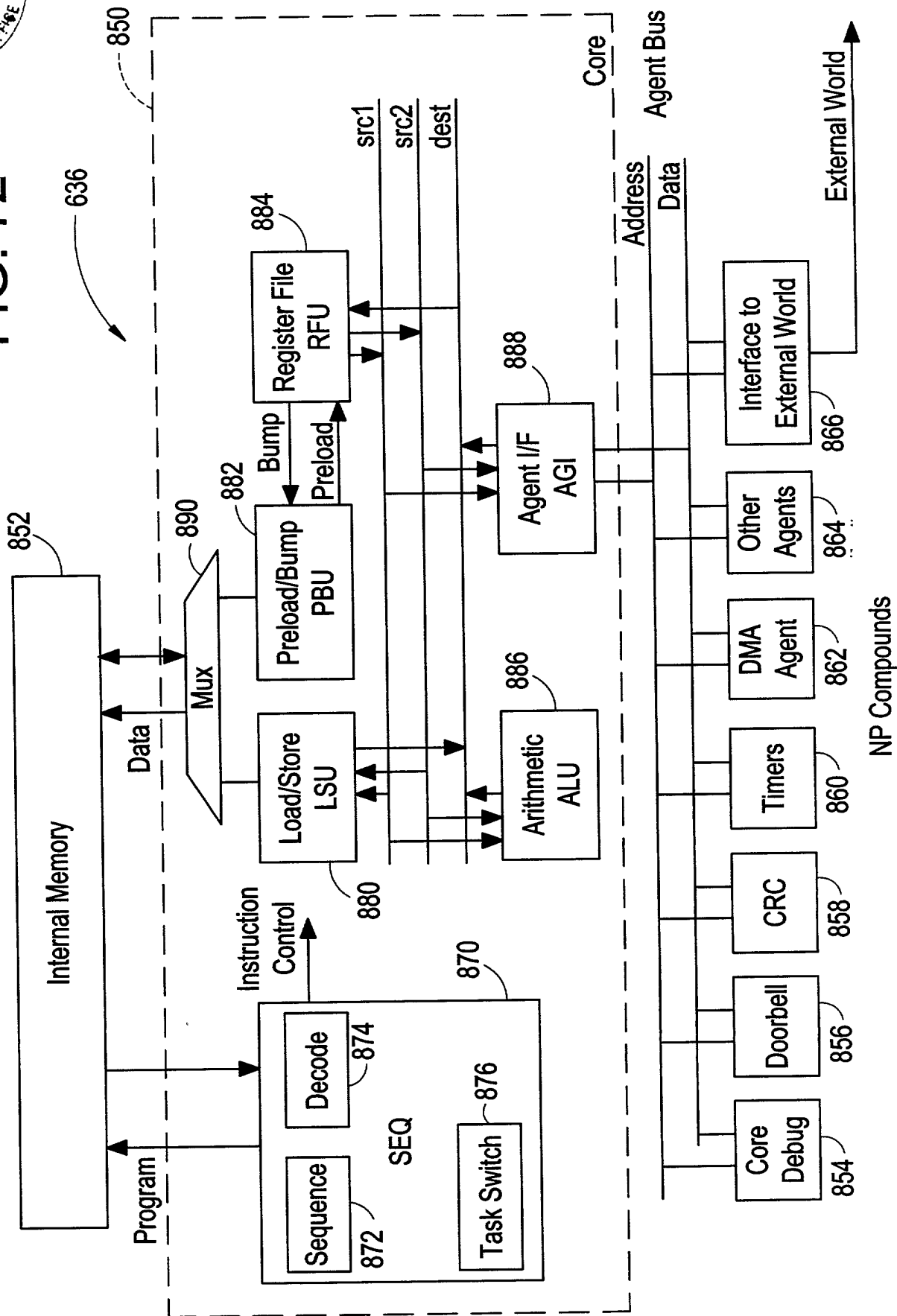


FIG. 72

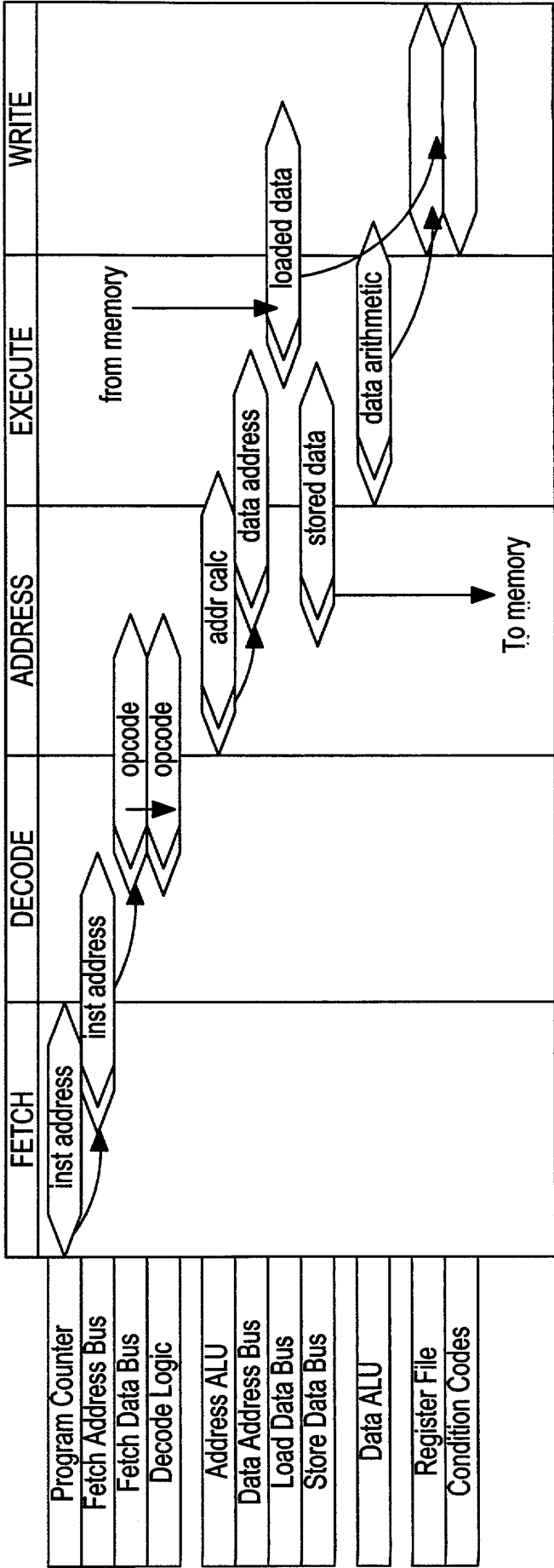
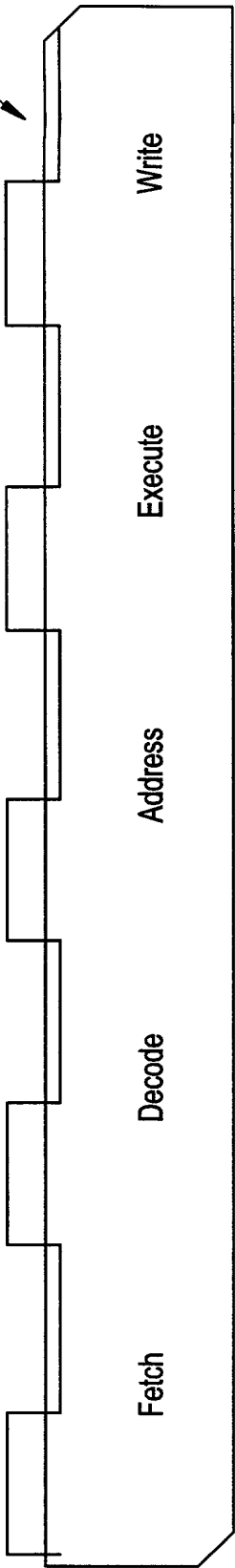




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FIG. 73

900



◊ - Flip Flop ◊ - Logic

FIG. 74

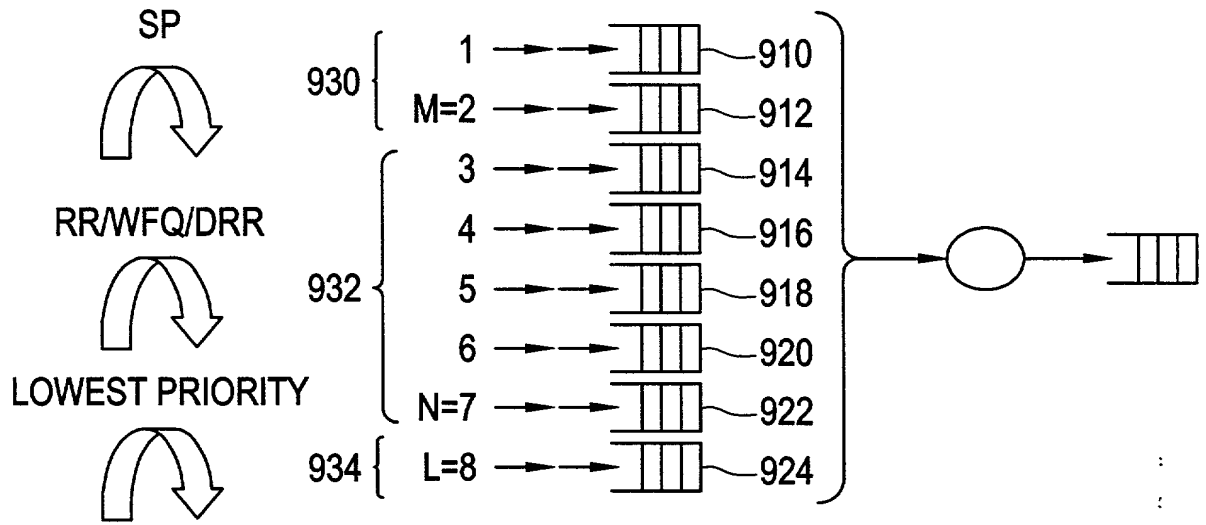


FIG. 75

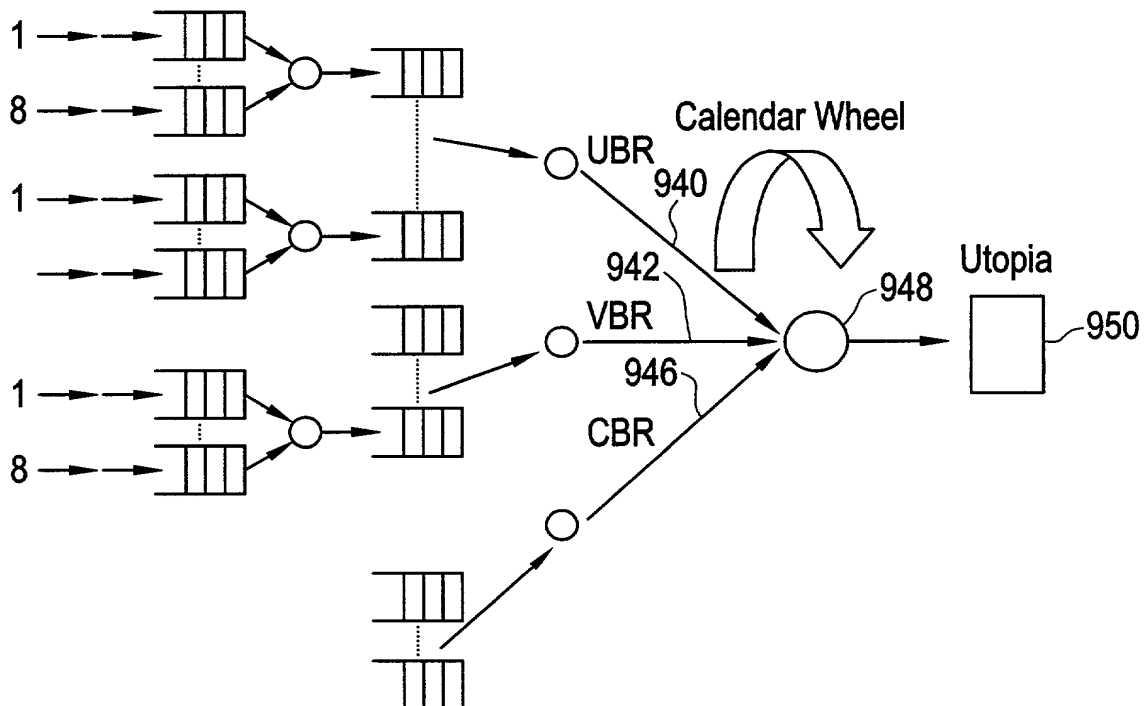


FIG. 76

960

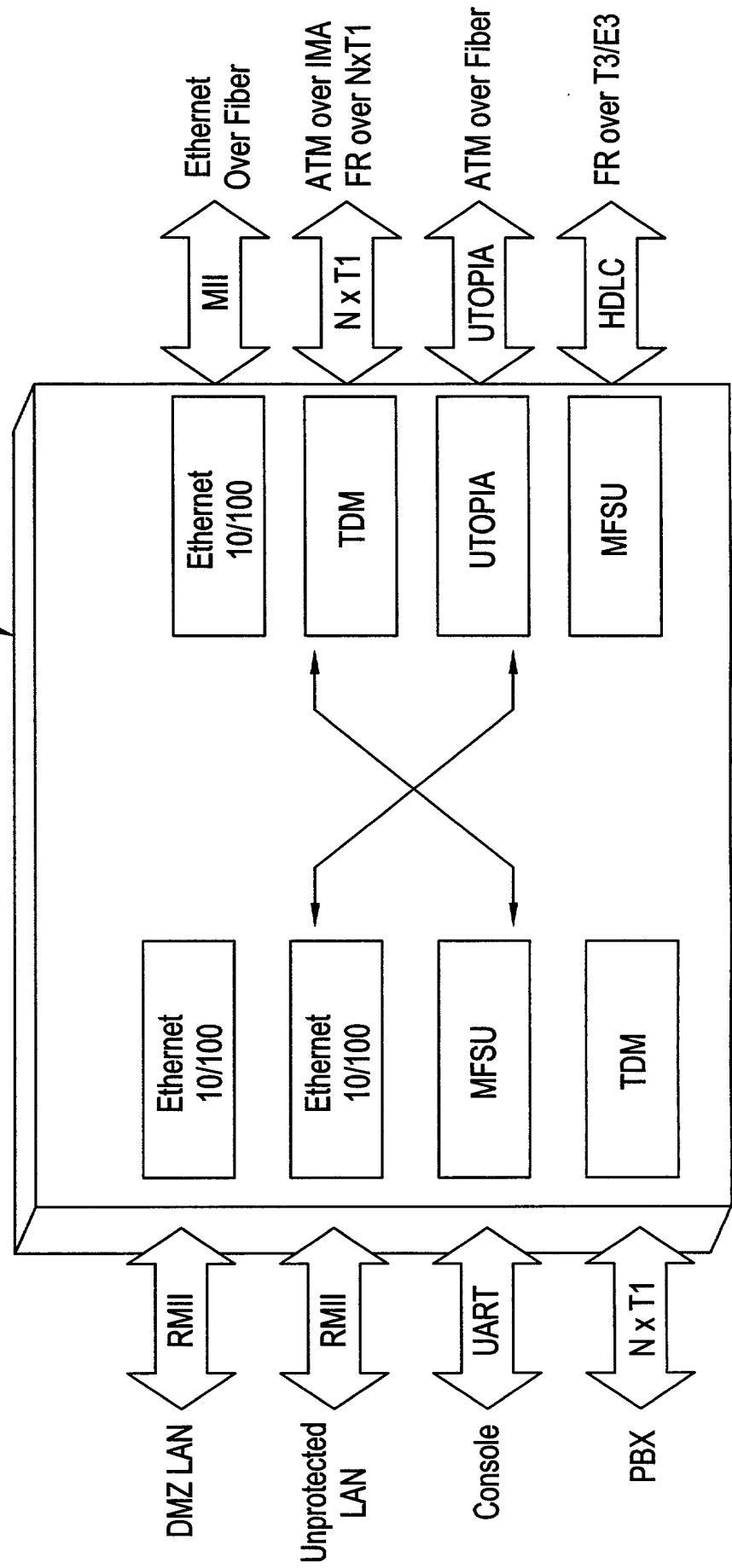


FIG. 77

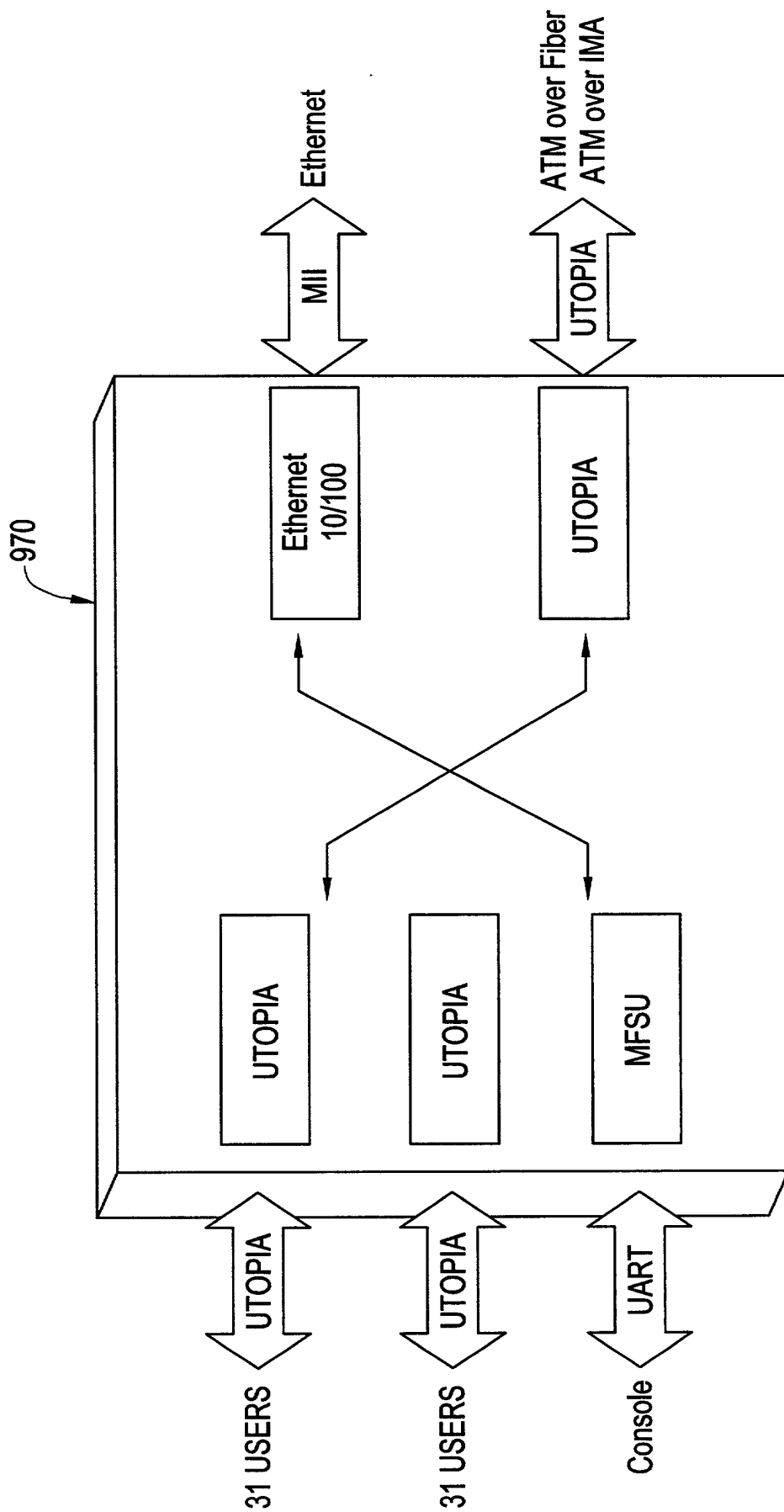


FIG. 78

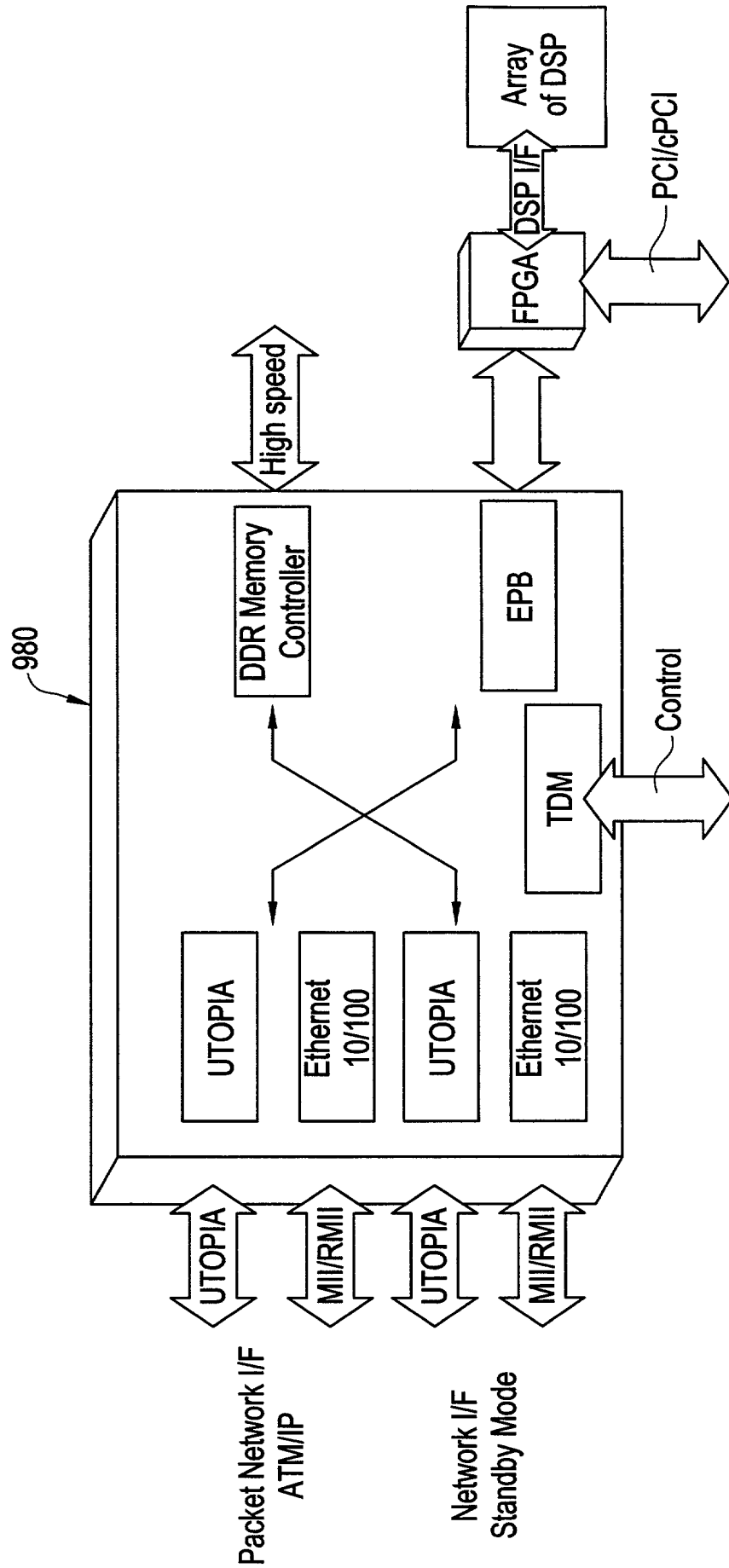


FIG. 79

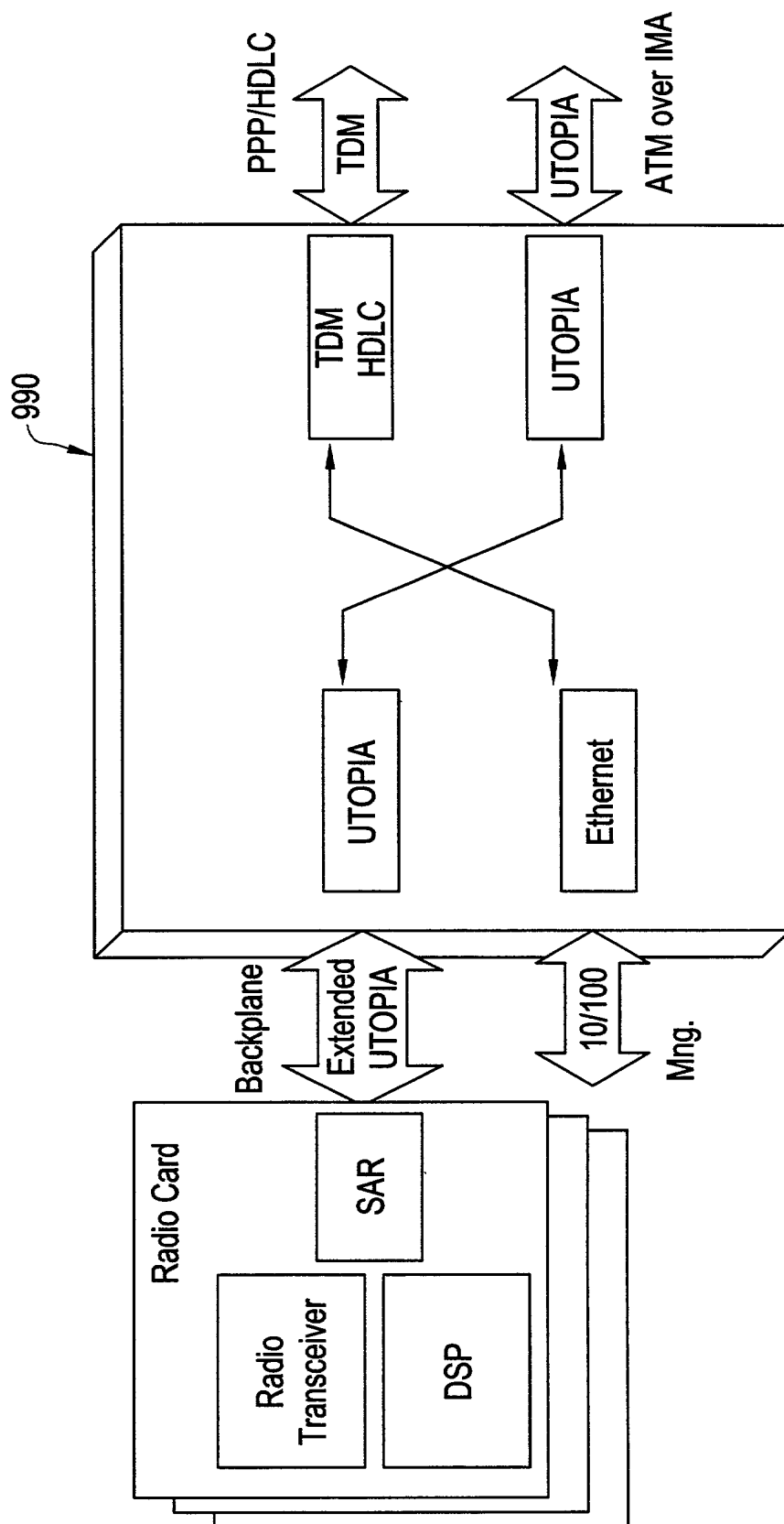


FIG. 80

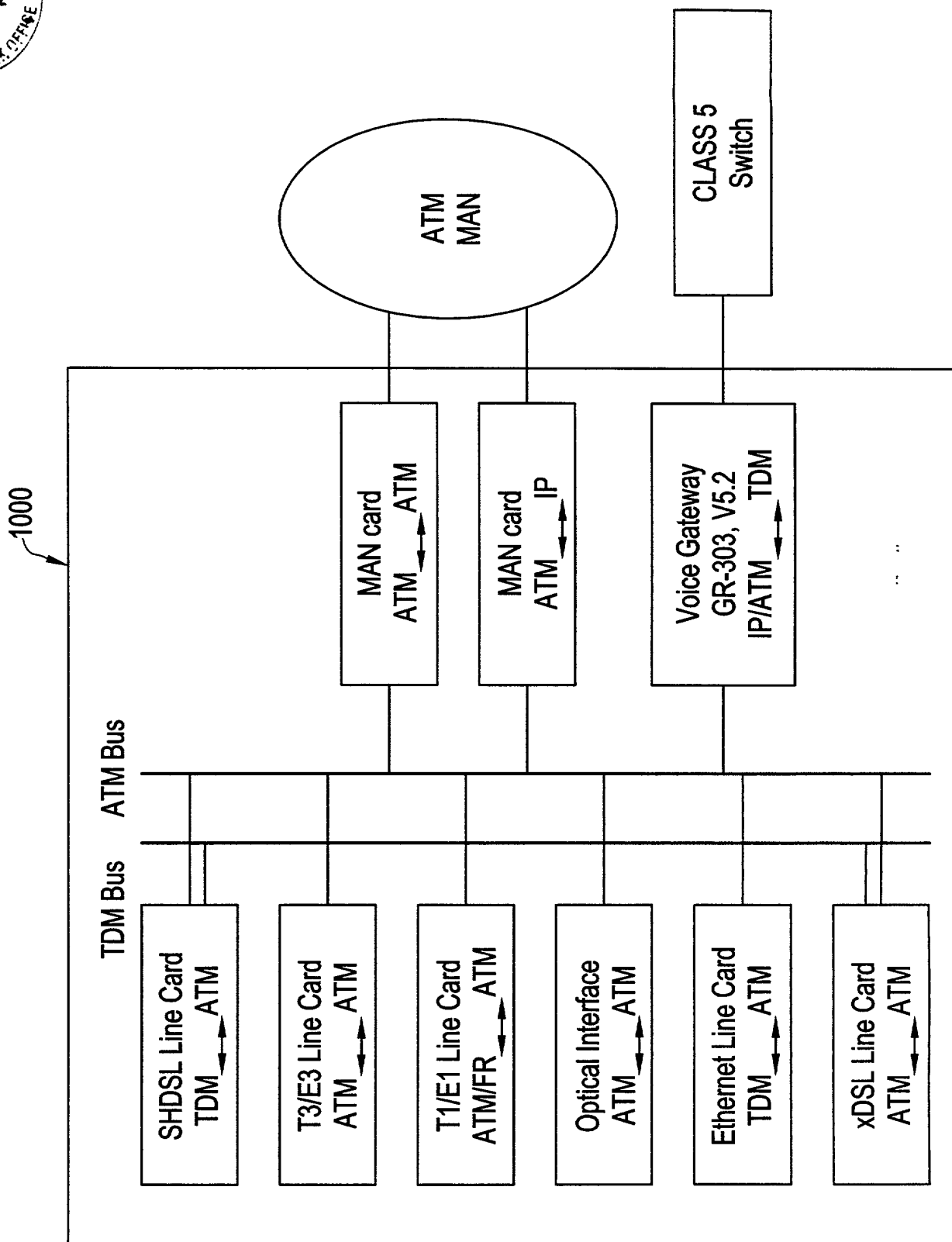


FIG. 81

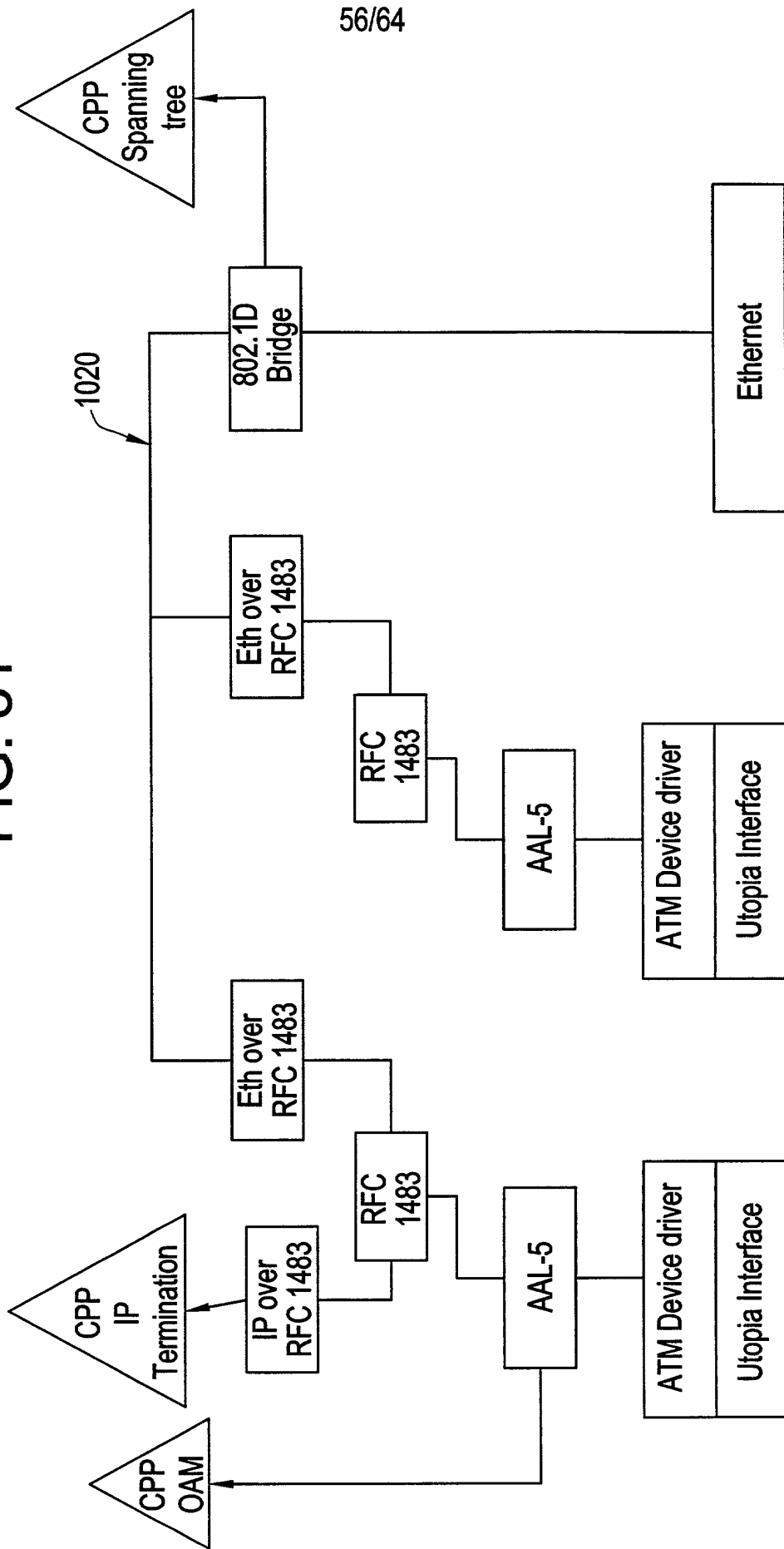


FIG. 82

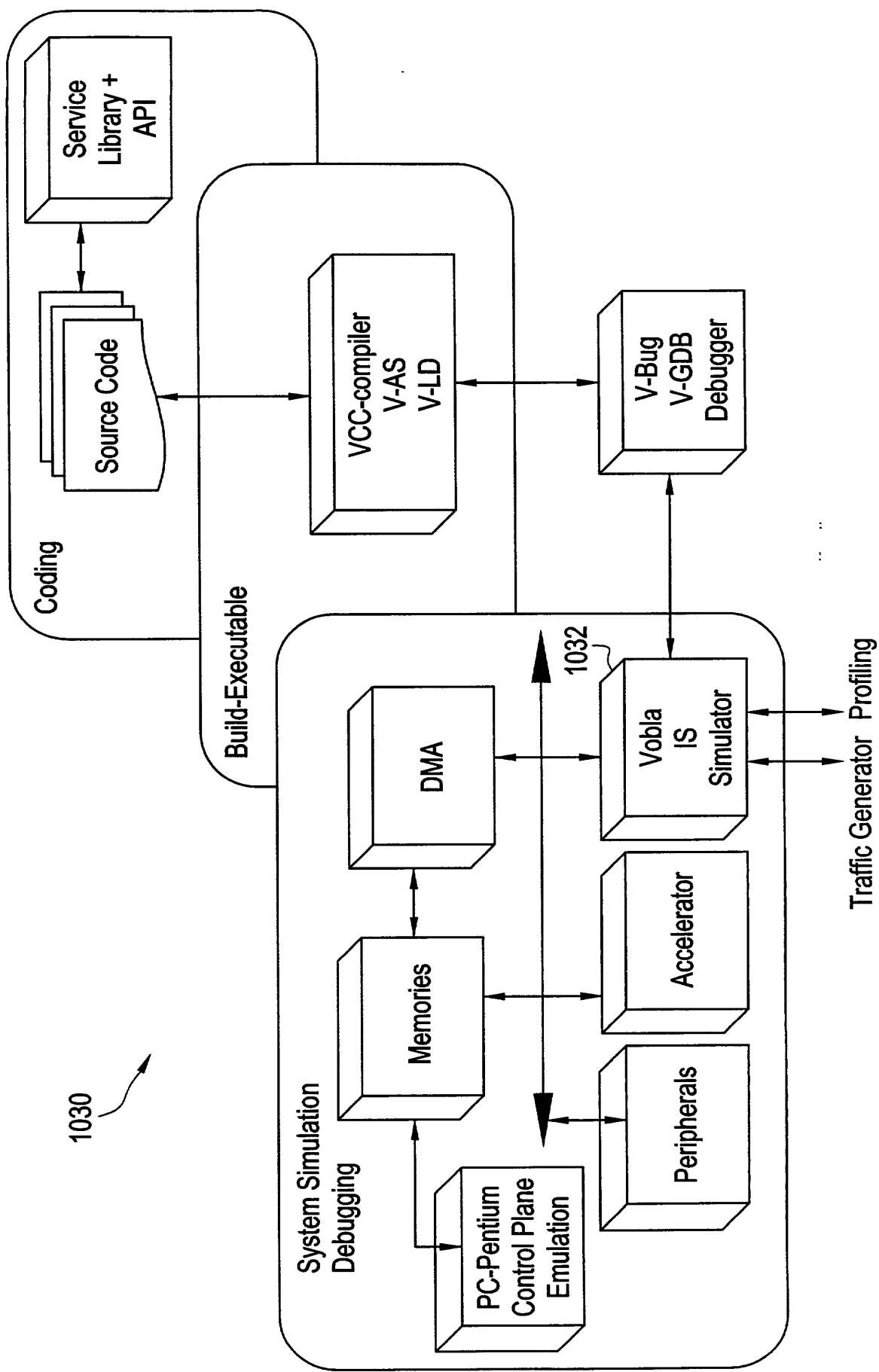


FIG. 83

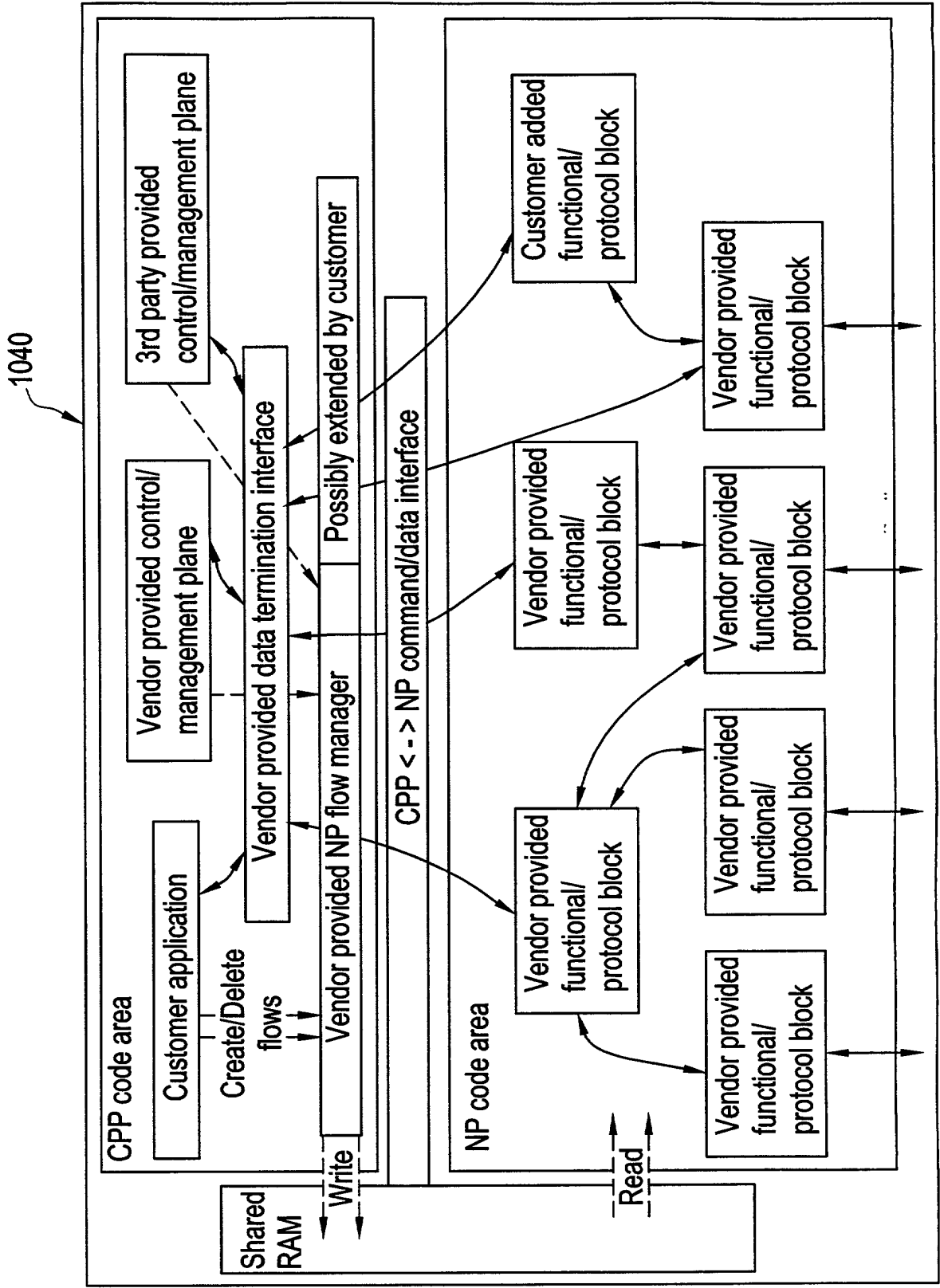


FIG. 84

1050

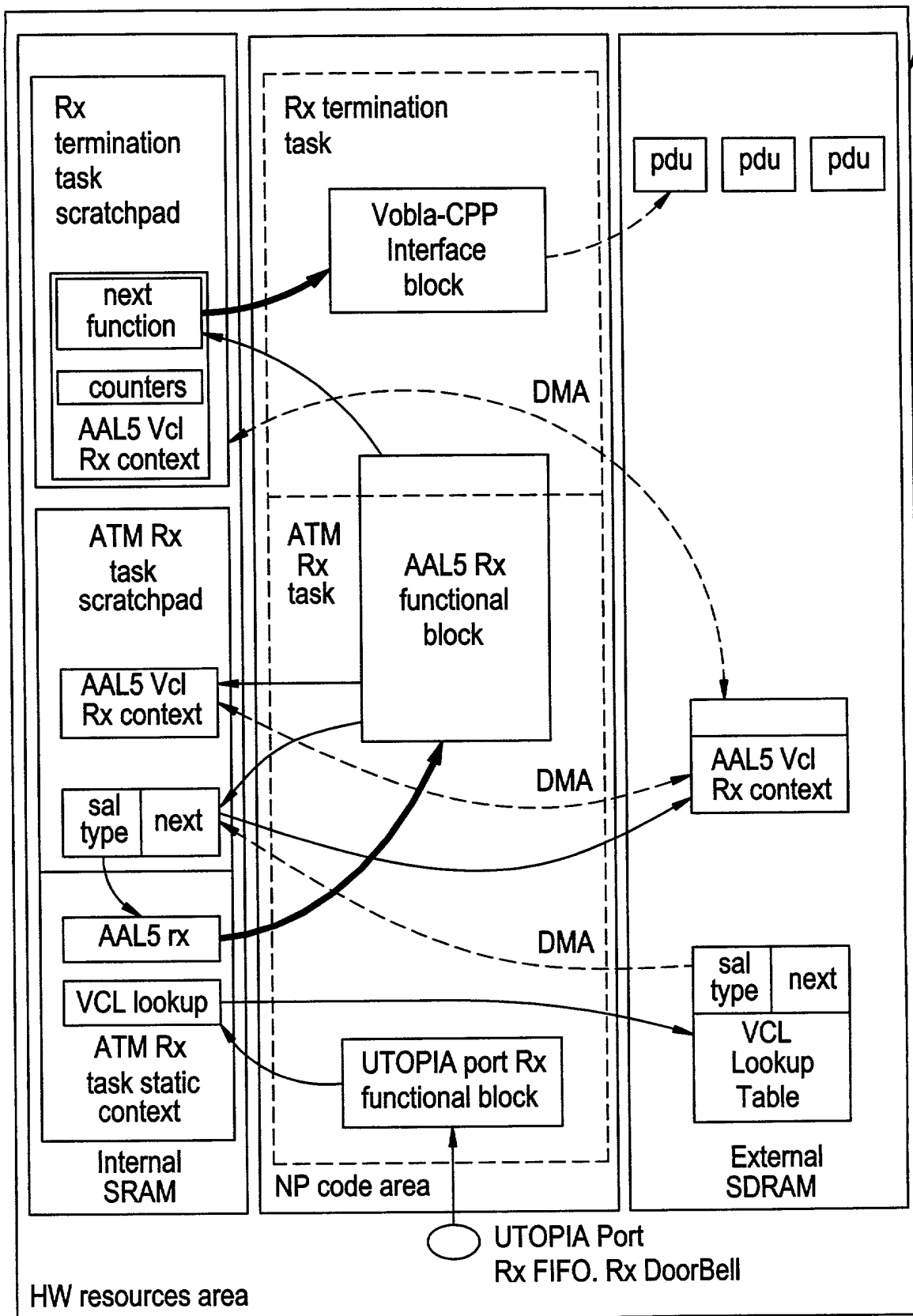


FIG. 85

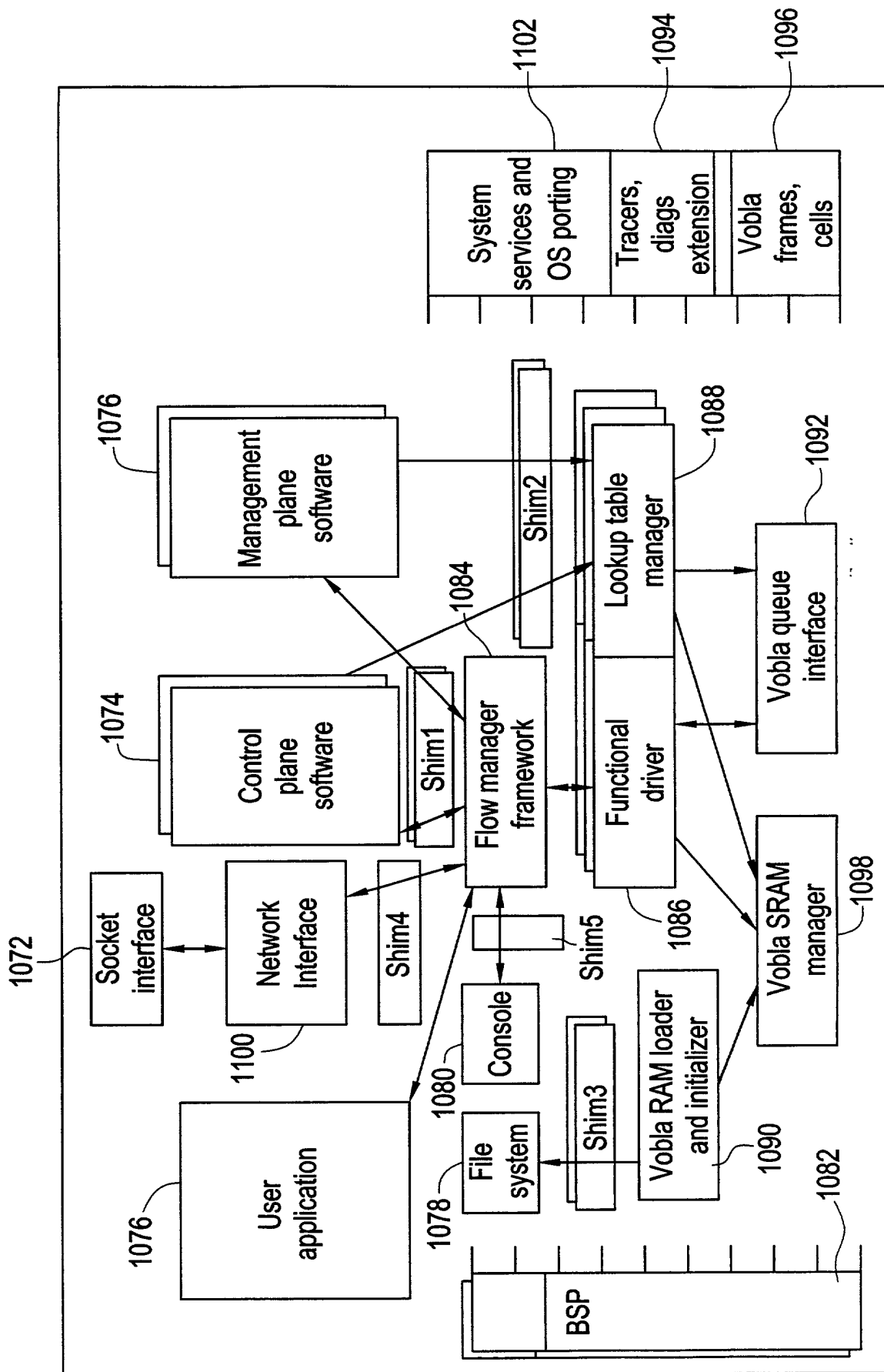
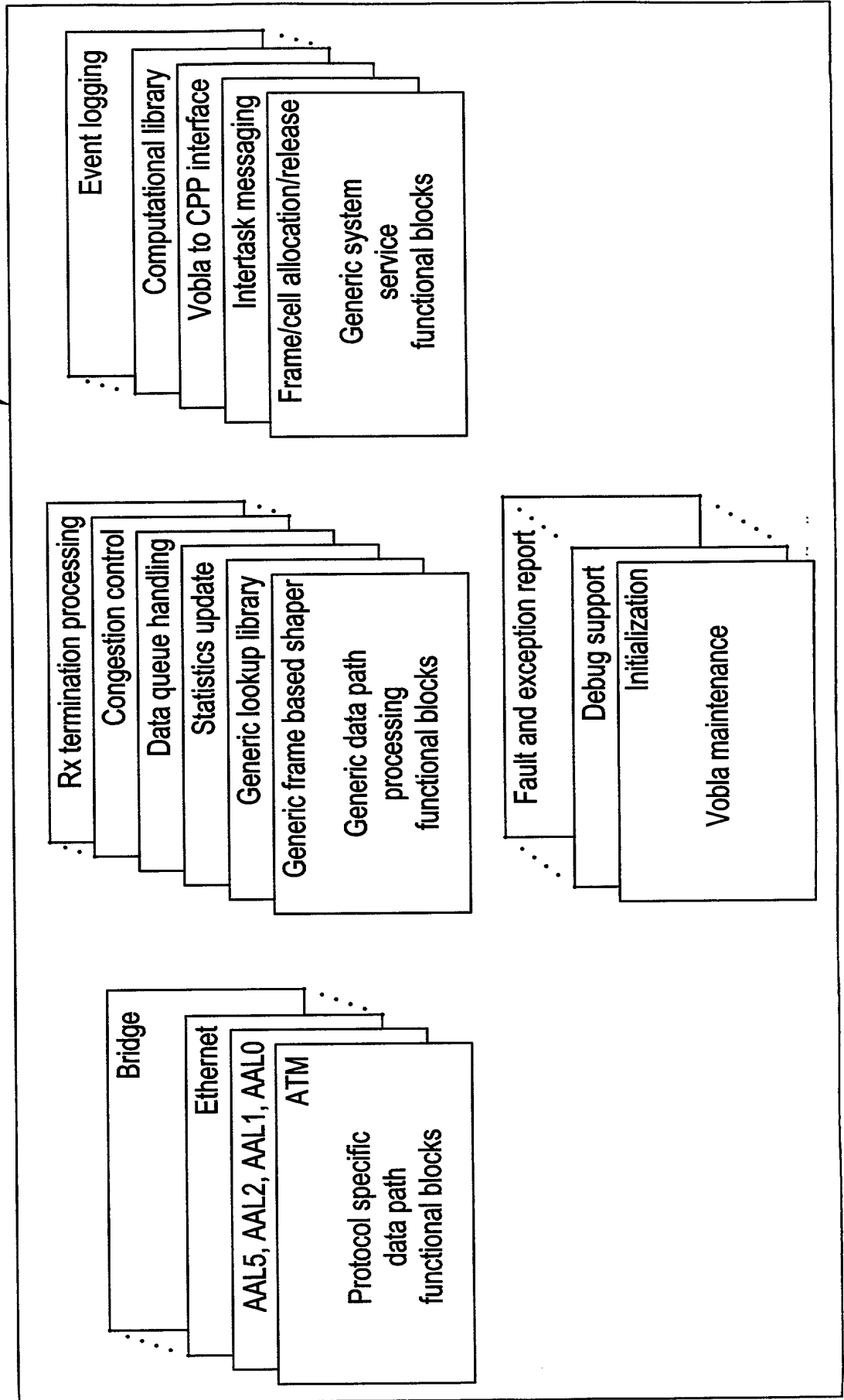


FIG. 86

1200





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FIG. 87

PRIOR ART

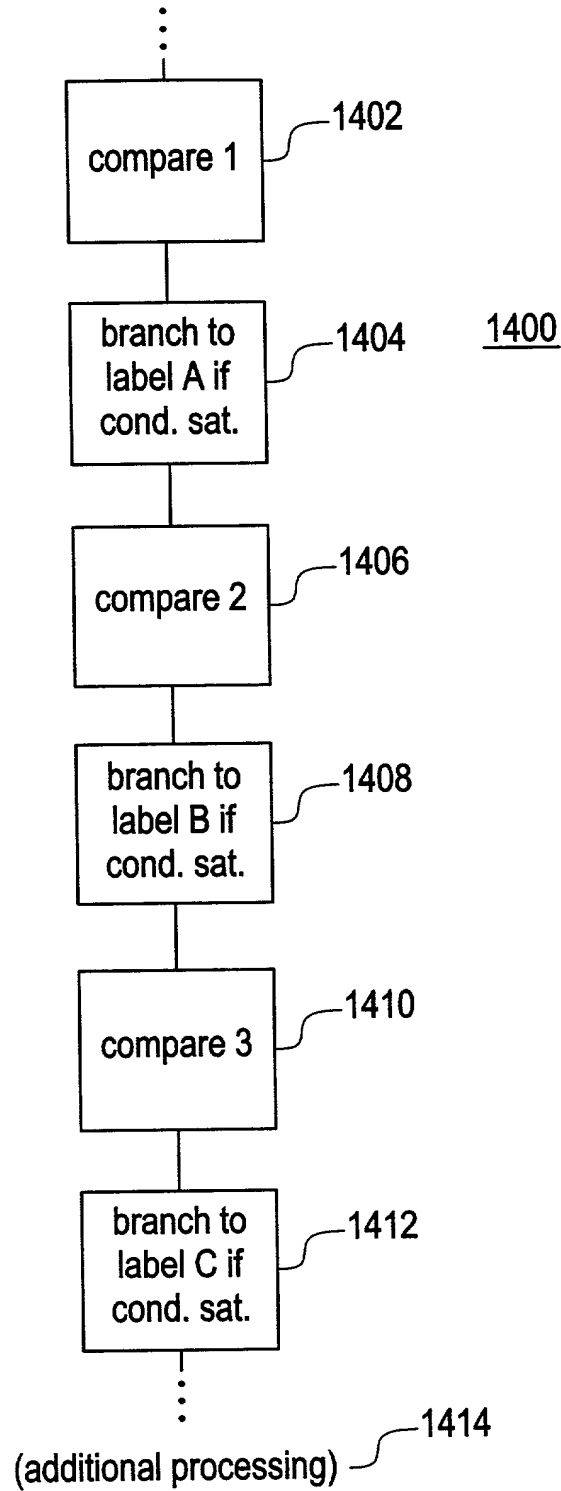


FIG. 88

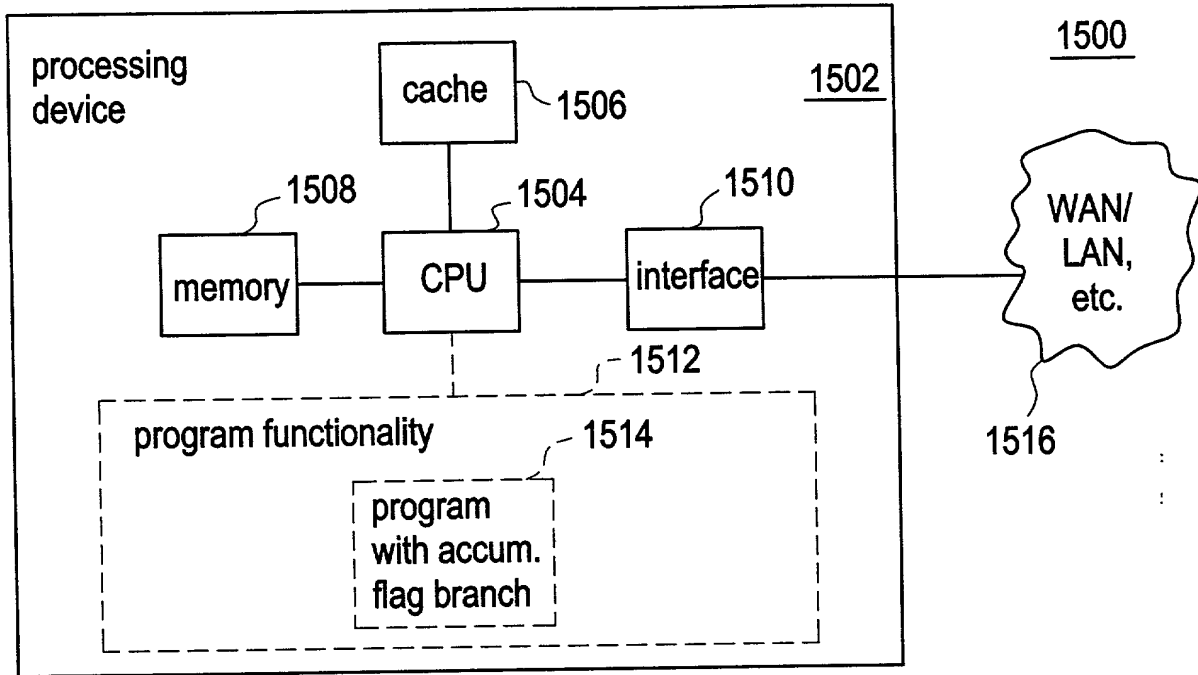


FIG. 89

